

FIG. 1
PRIOR ART

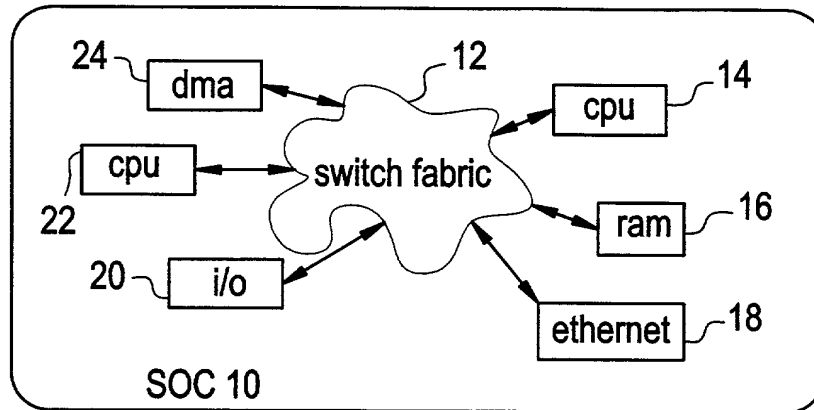


FIG. 2

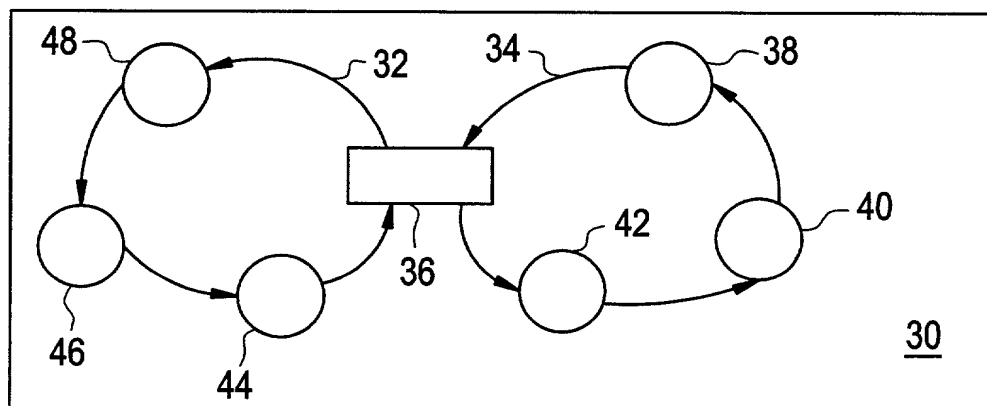


FIG. 3

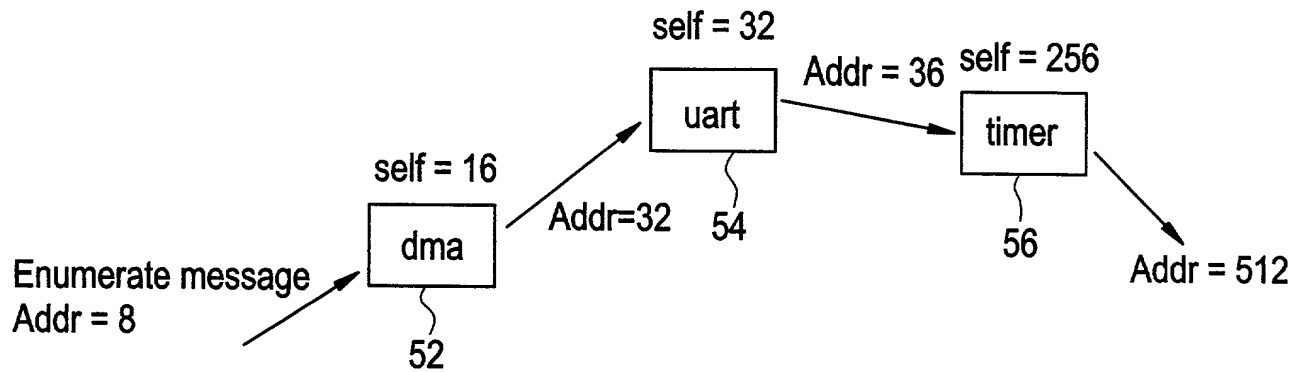


FIG. 4

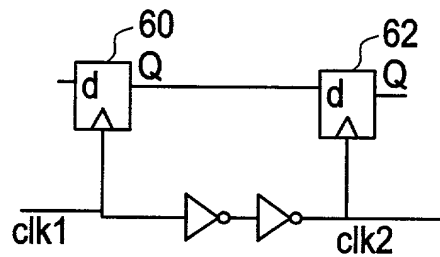


FIG. 5

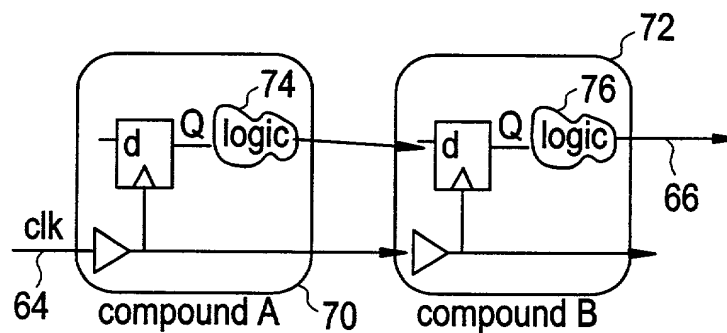


FIG. 6

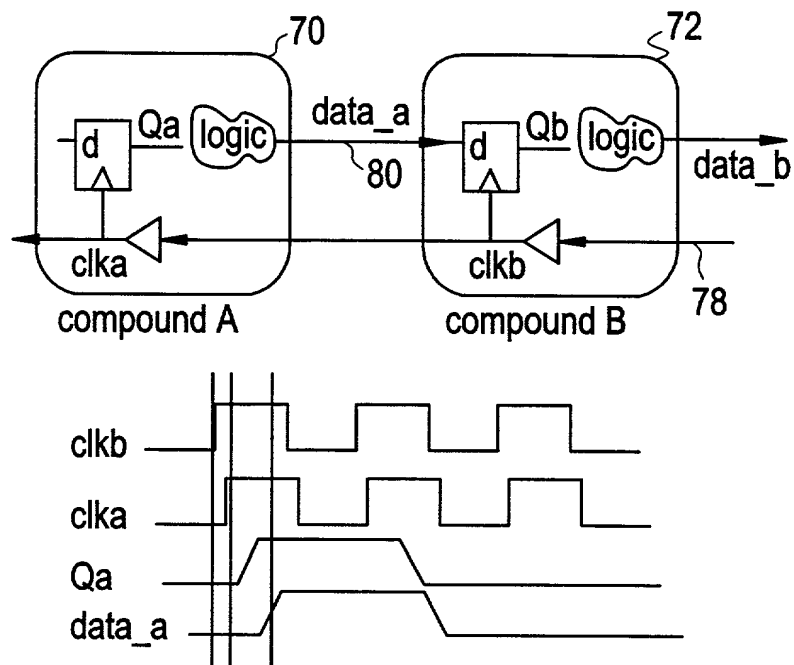


FIG. 7

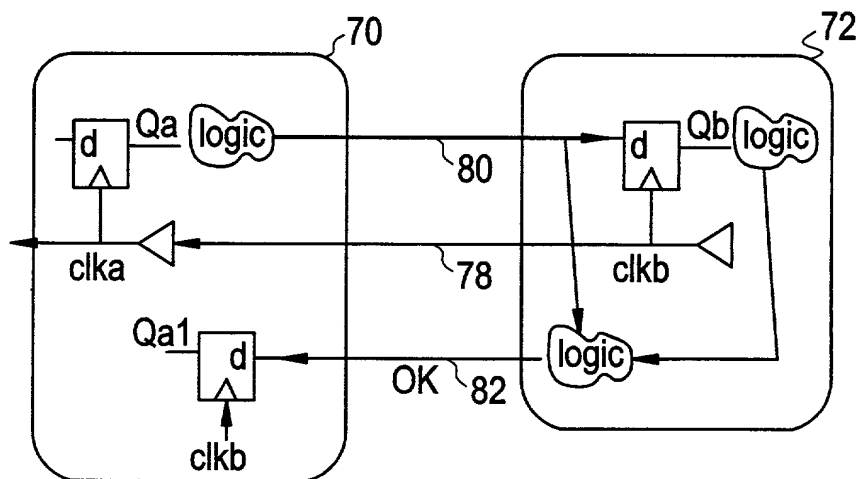


FIG. 8

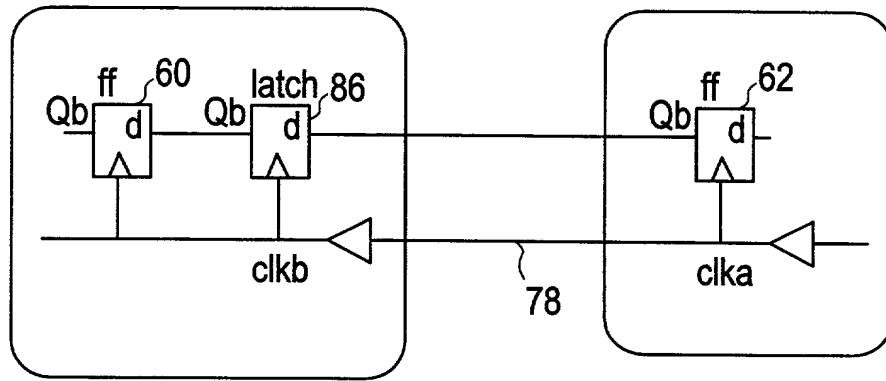


FIG. 9

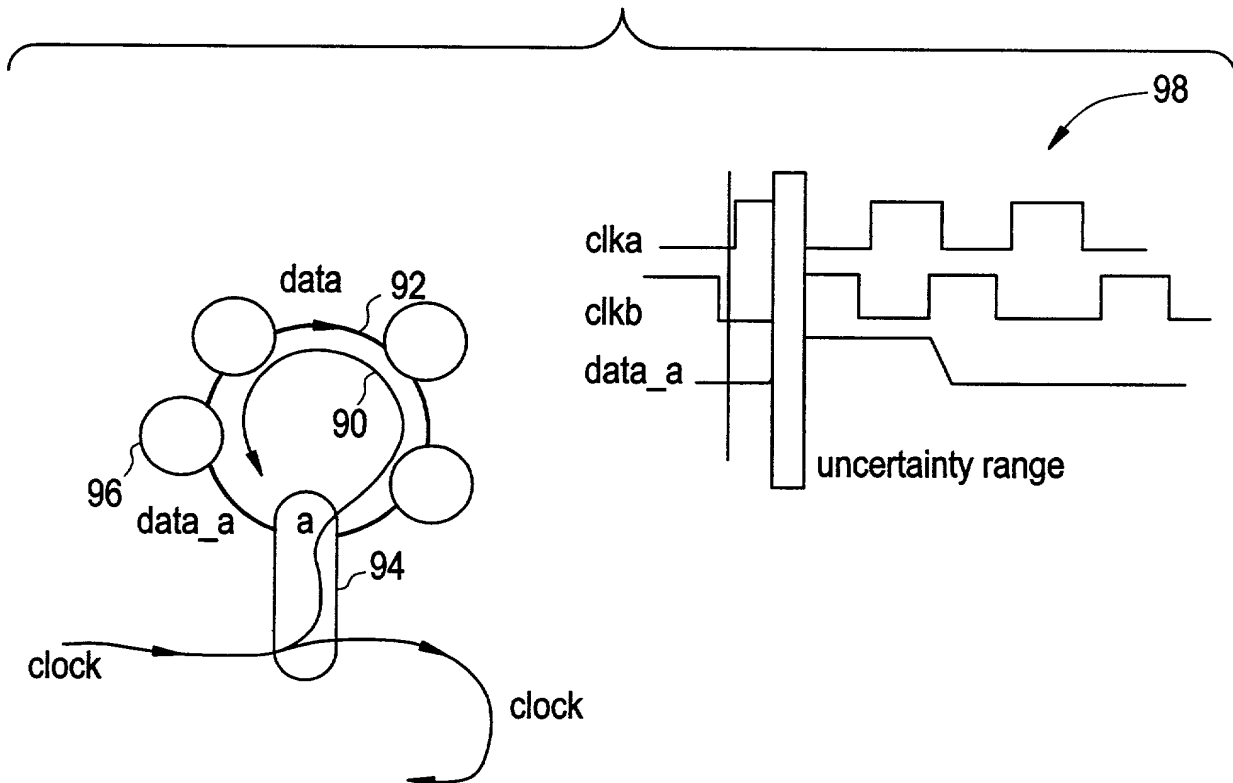


FIG. 10

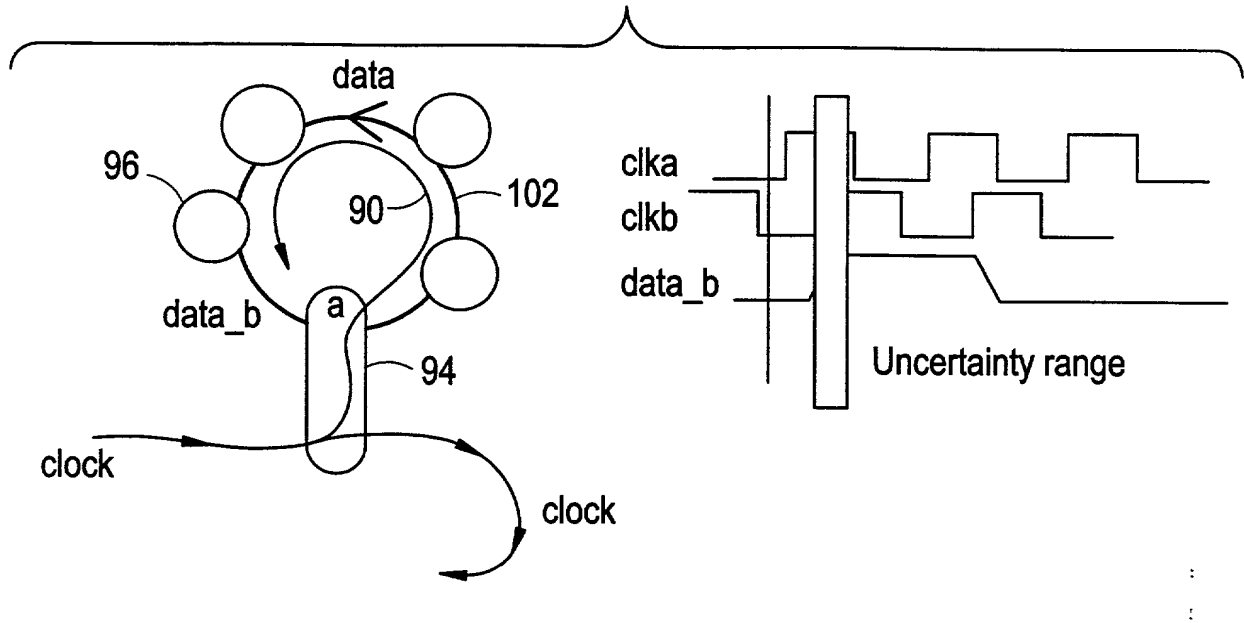


FIG. 11

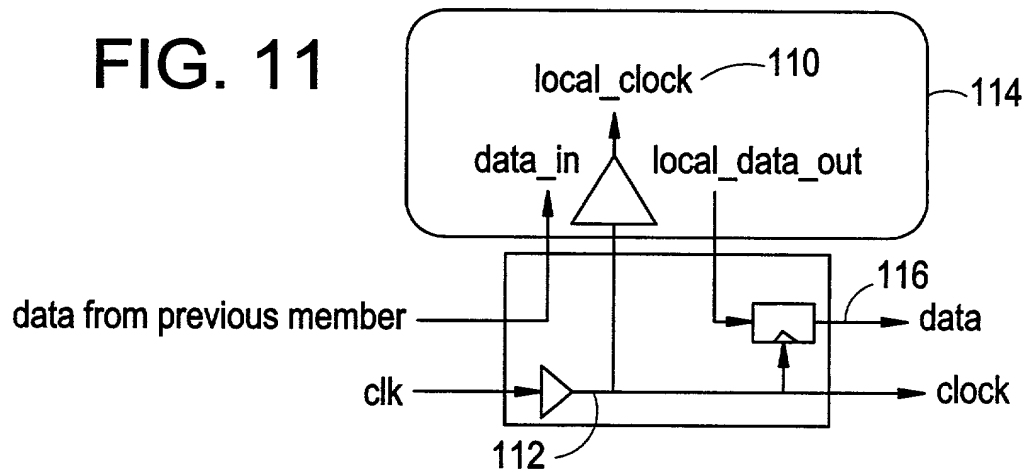


FIG. 12

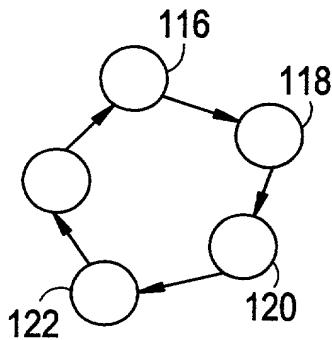


FIG. 13

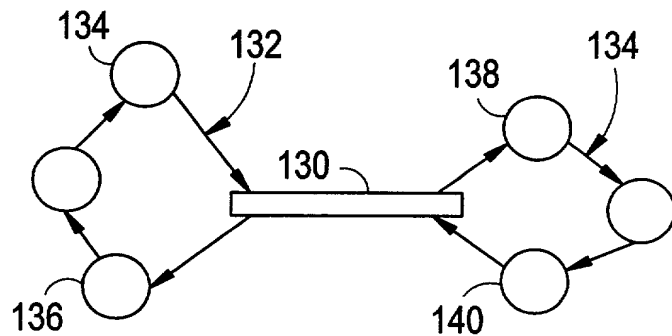


FIG. 14

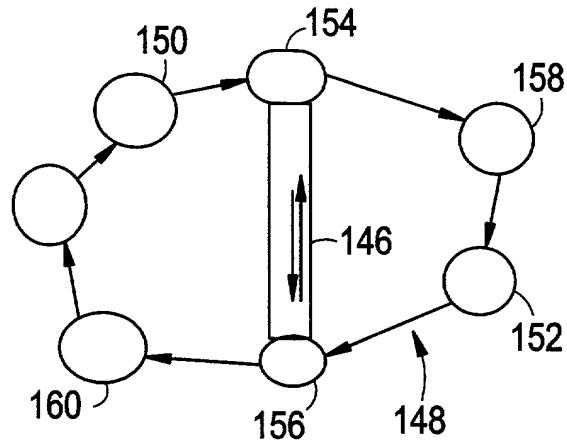


FIG. 15

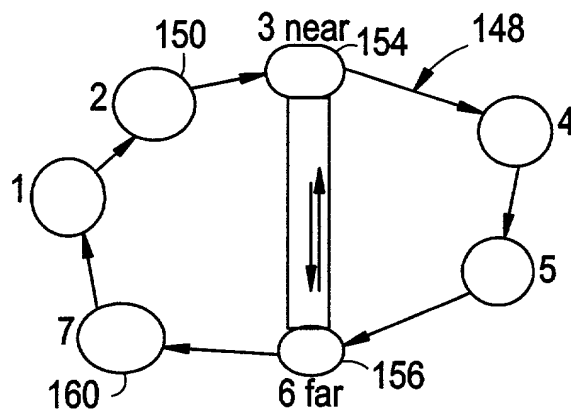


FIG. 16

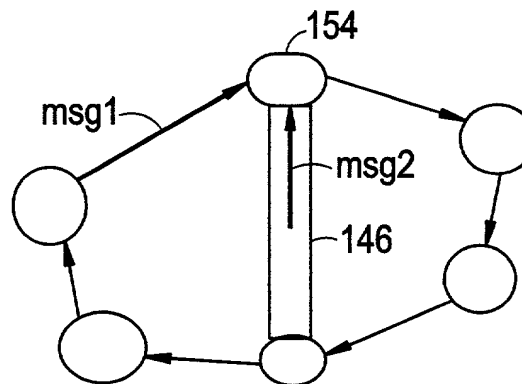


FIG. 17

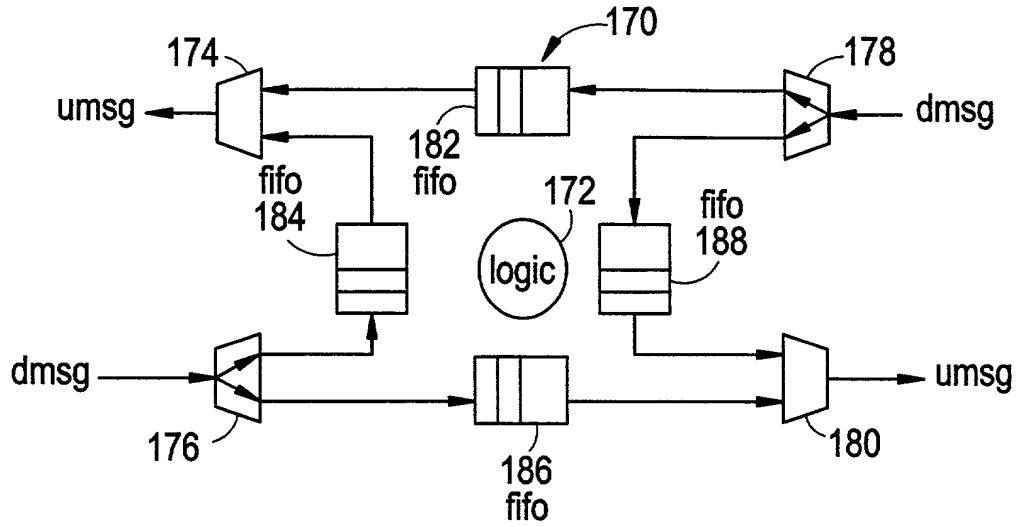


FIG. 18

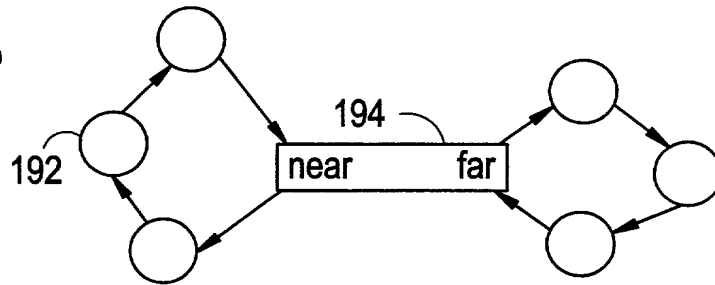


FIG. 19

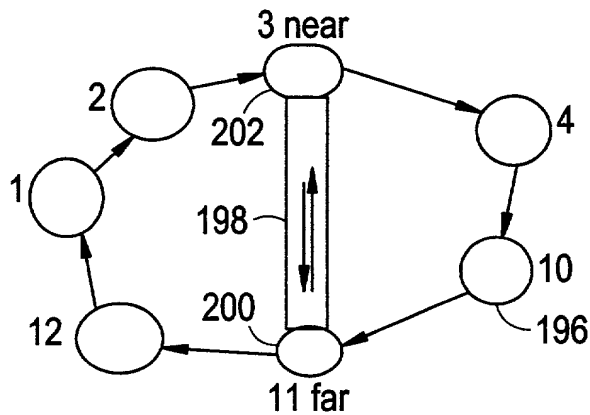


FIG. 20

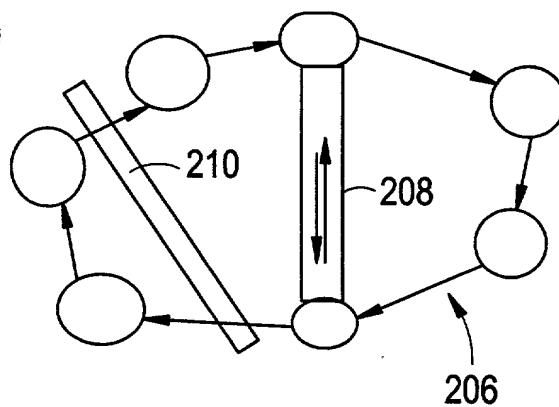


FIG. 21

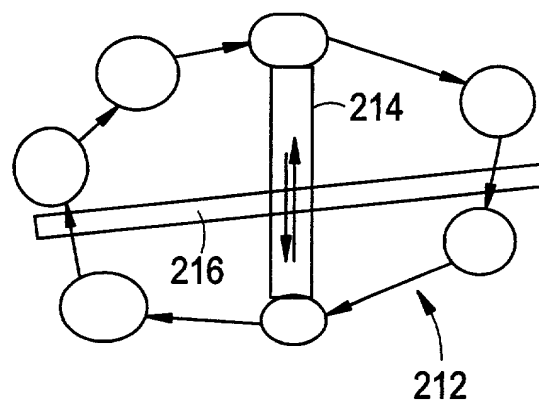


FIG. 22

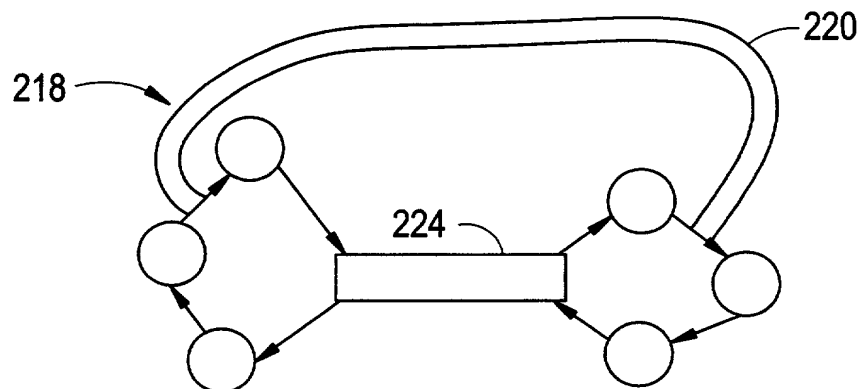


FIG. 23

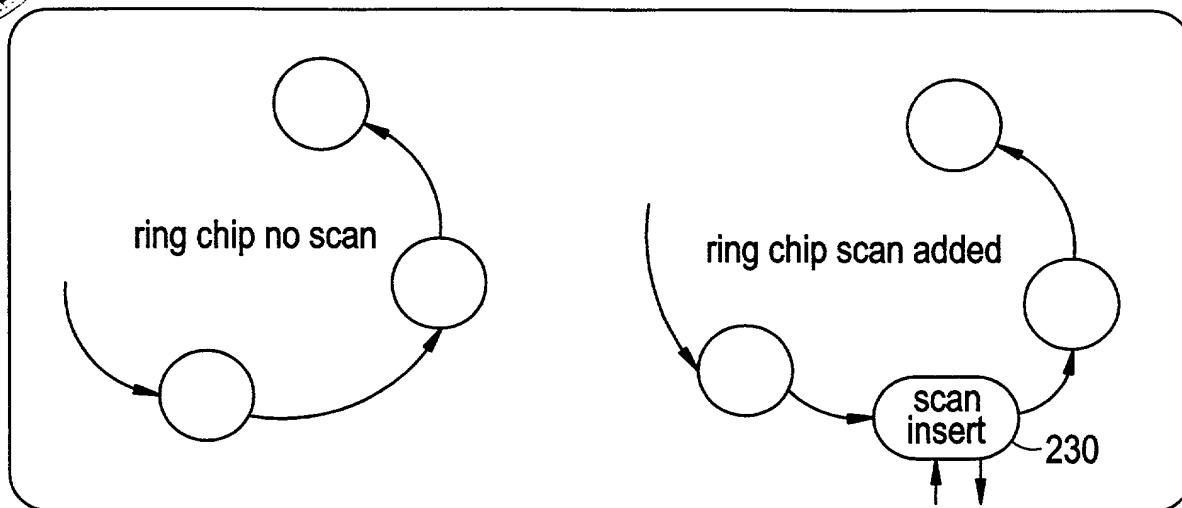


FIG. 24

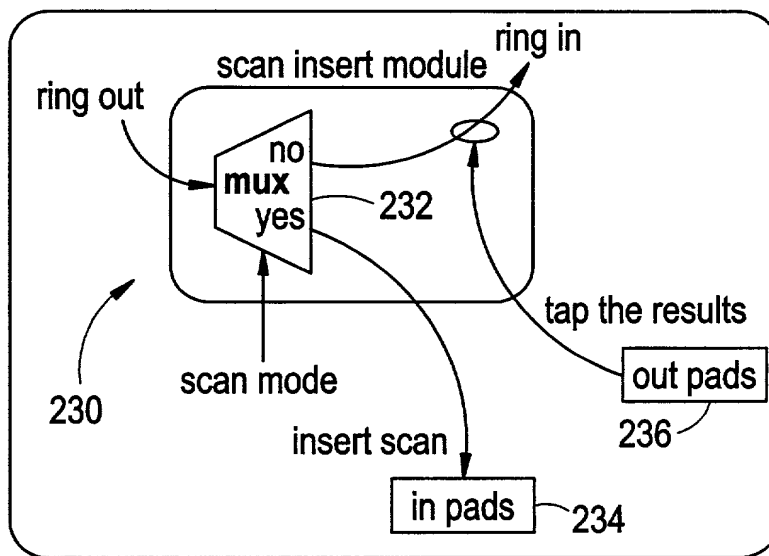


FIG. 25

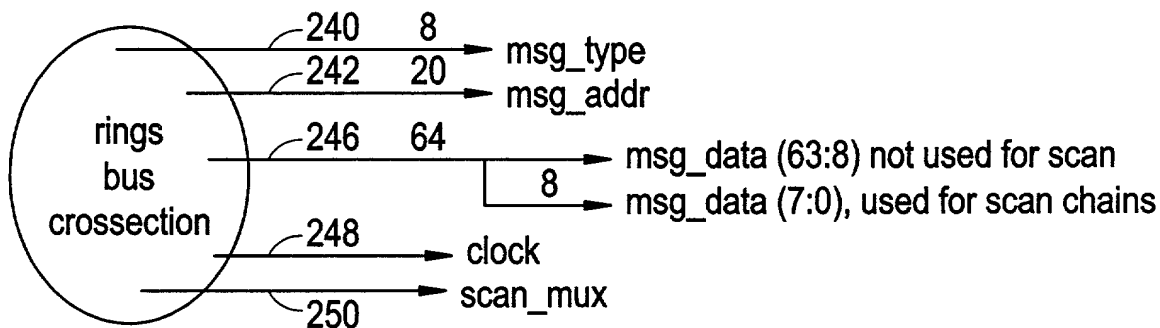


FIG. 26

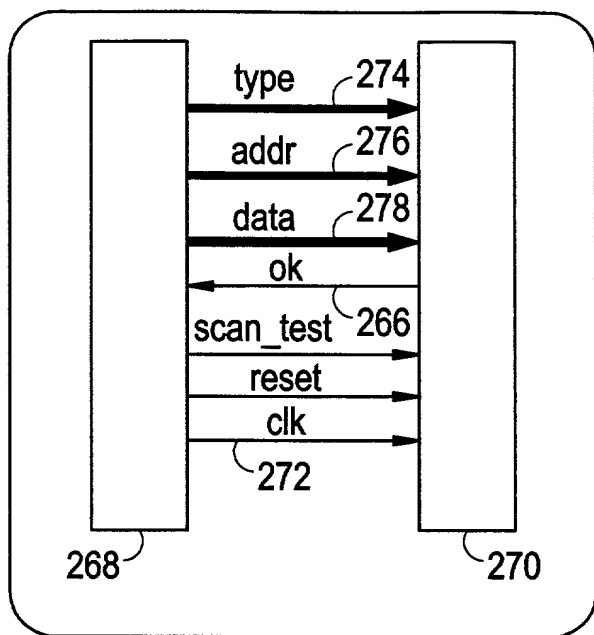
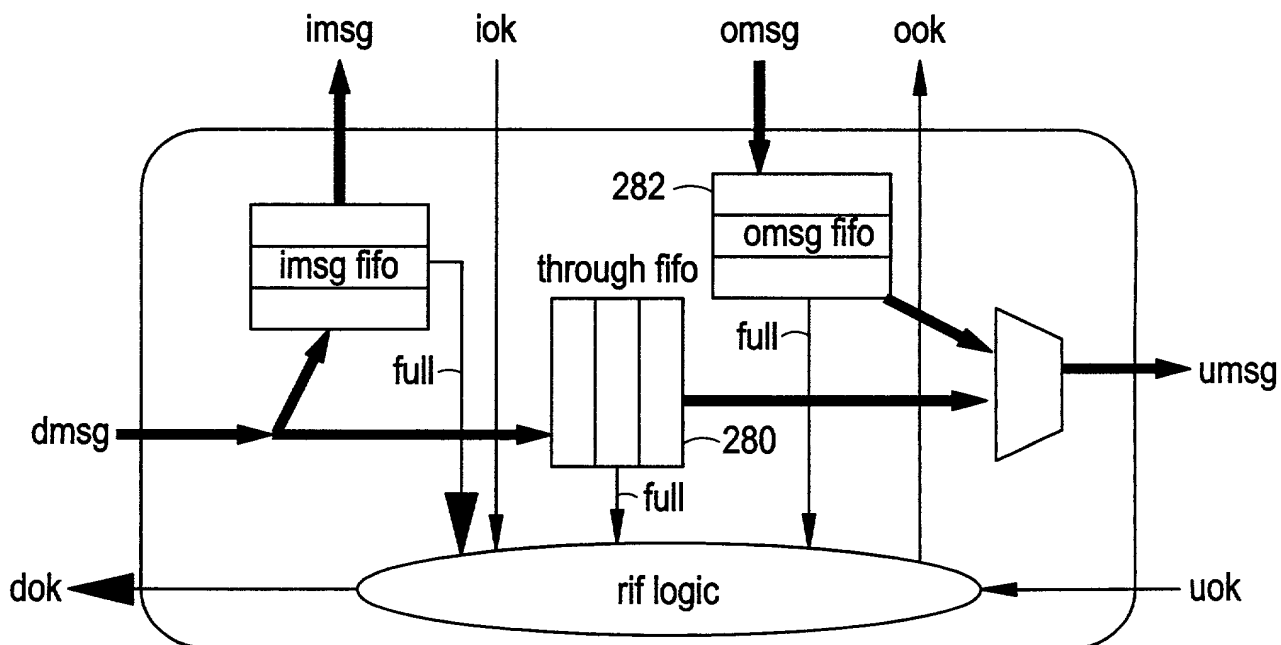


FIG. 27



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FIG. 28

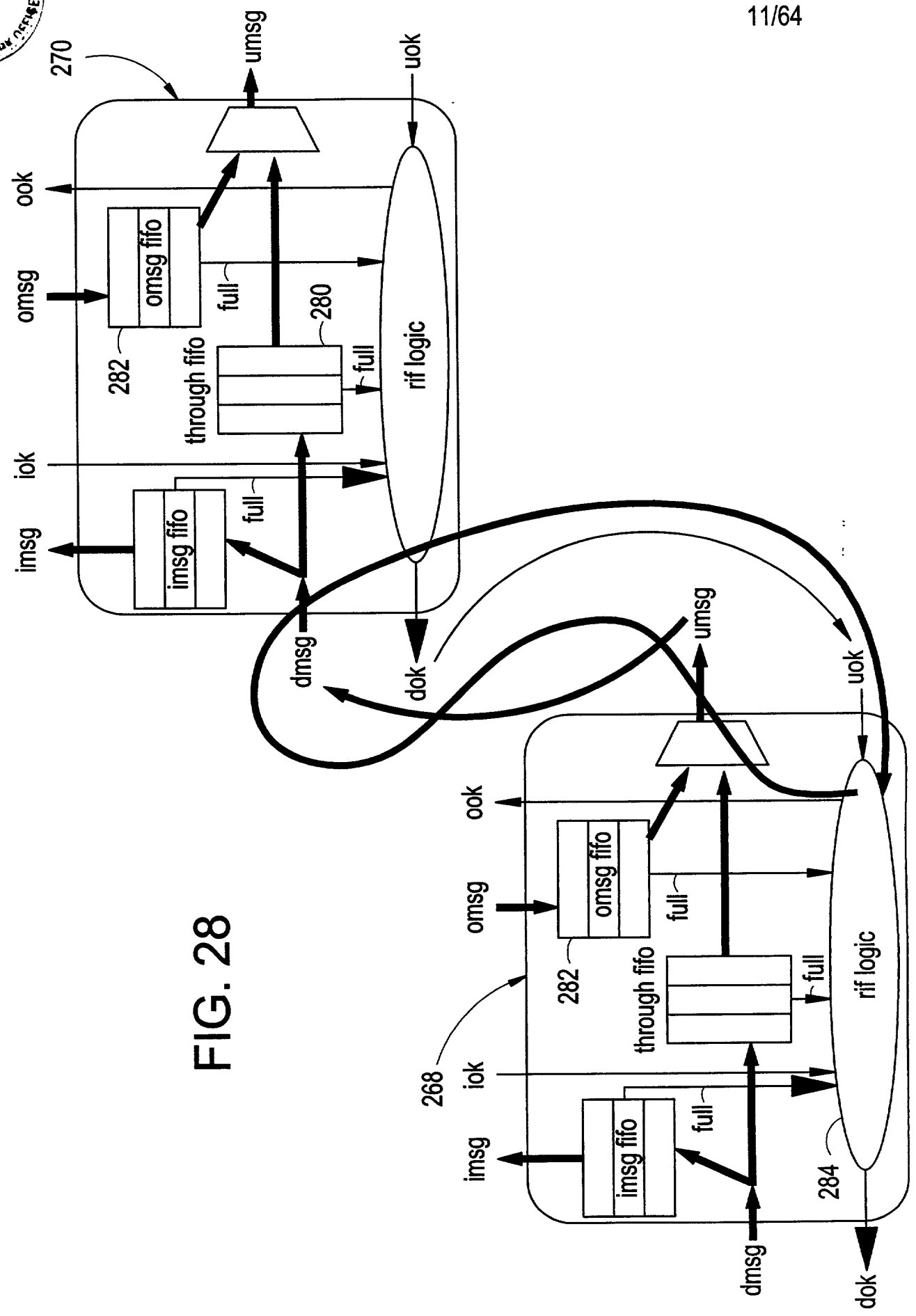
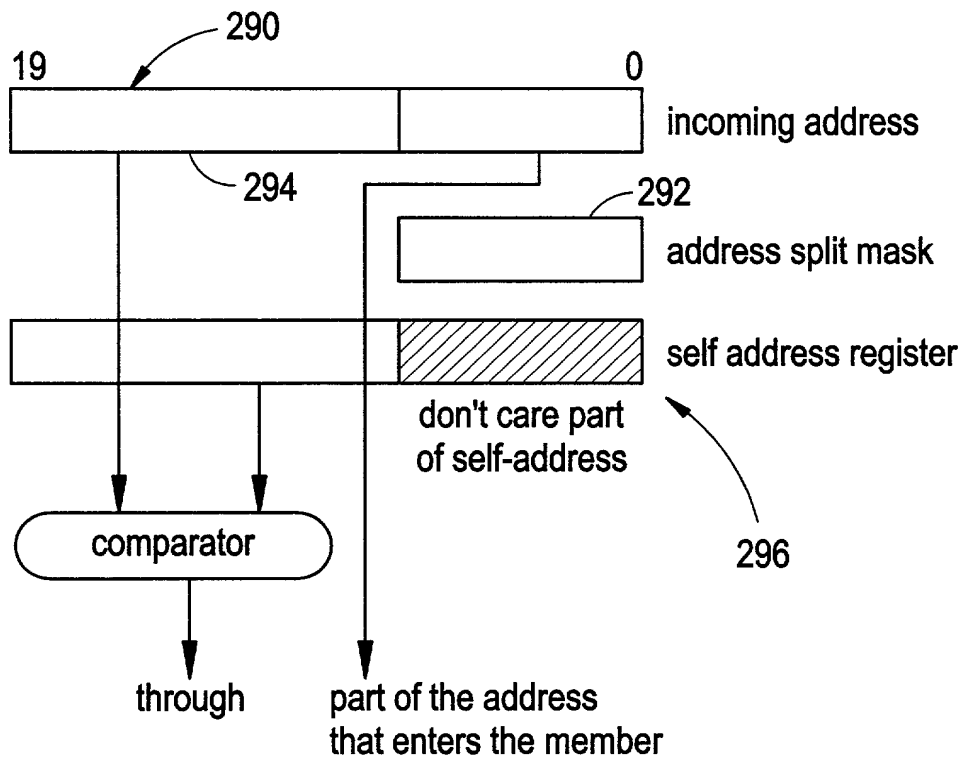


FIG. 29



20020926 "The Epoch"

FIG. 30

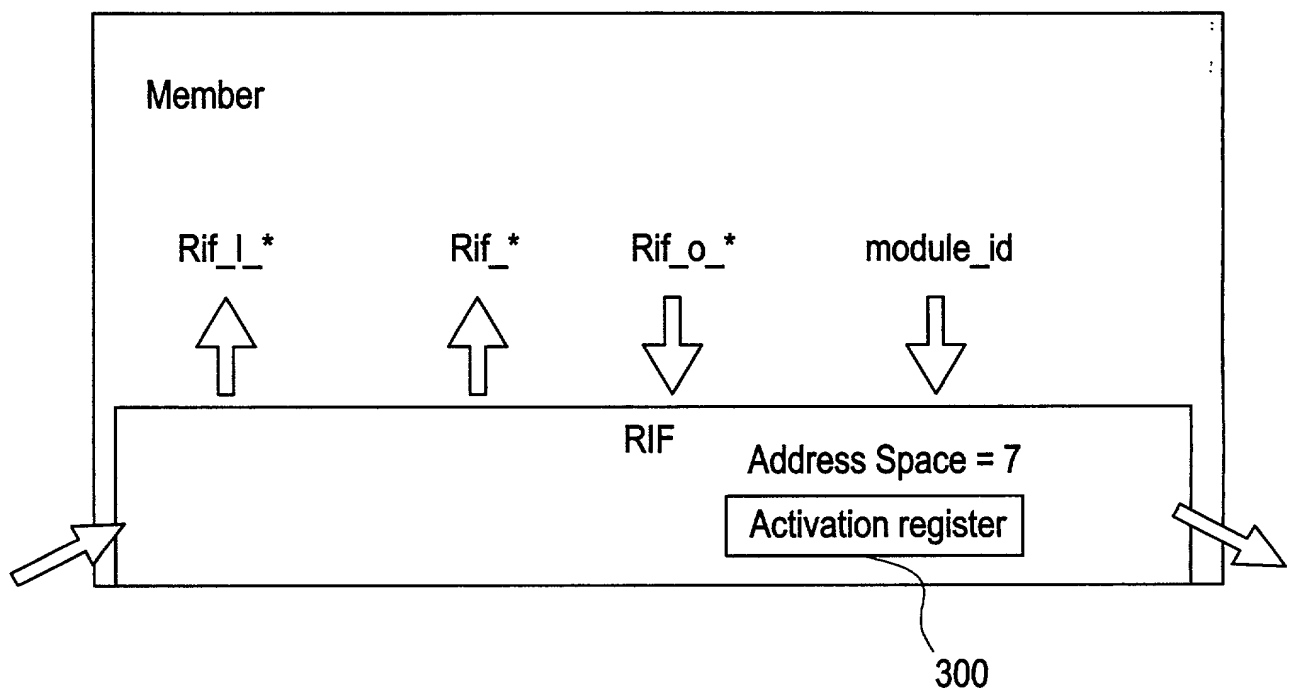


FIG. 31

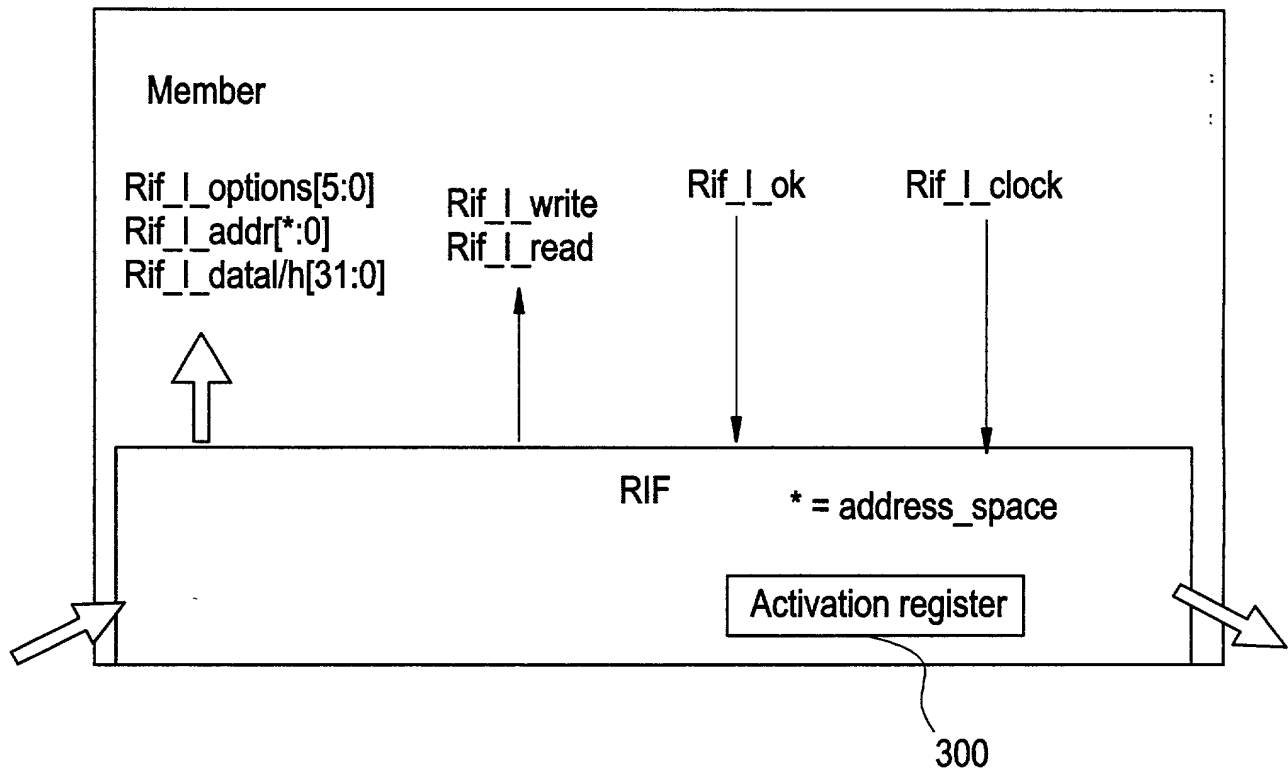


FIG. 32

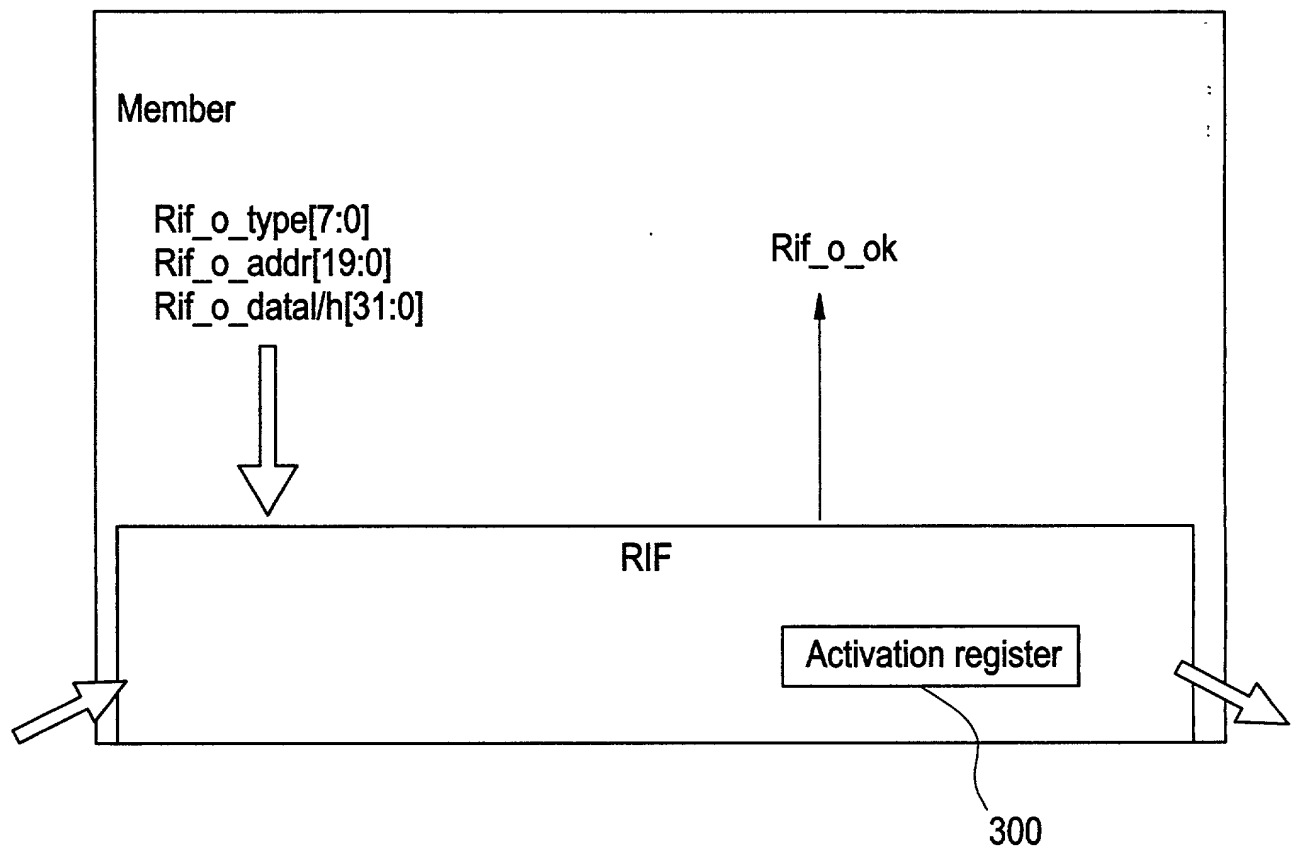


FIG. 33

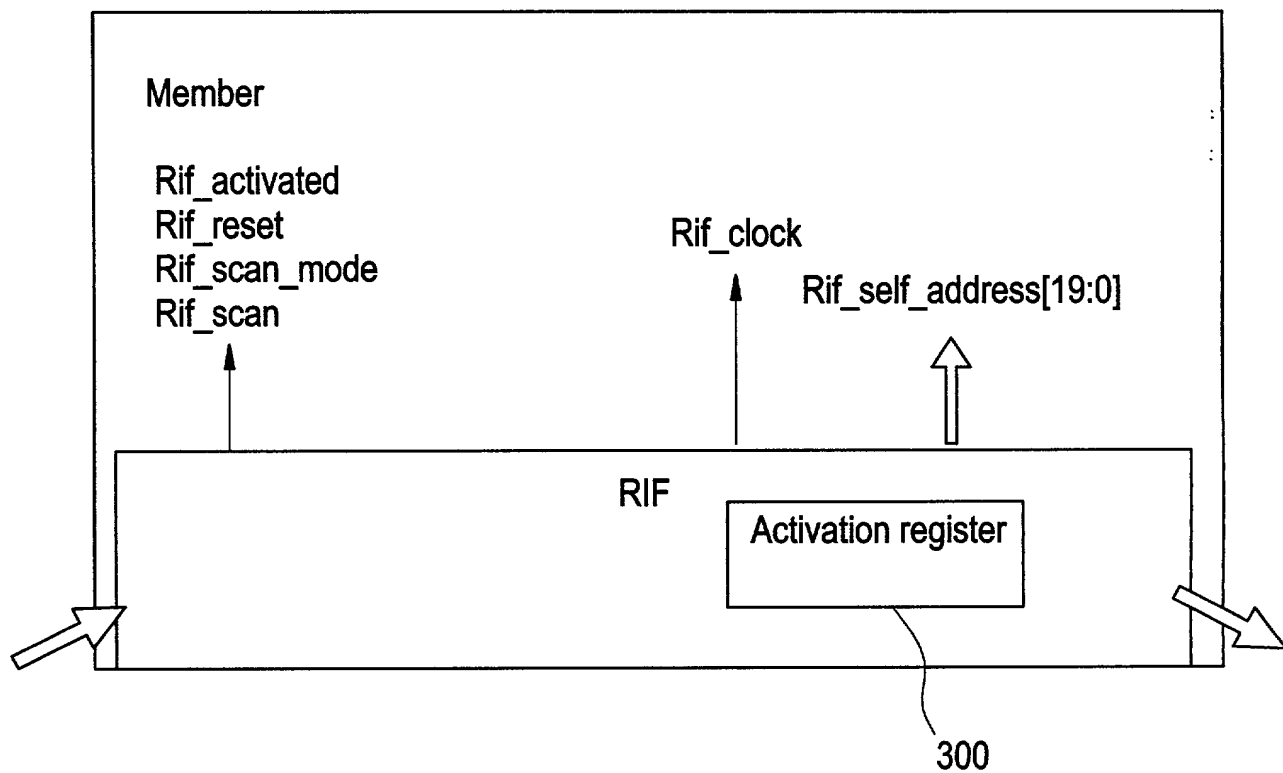


FIG. 34

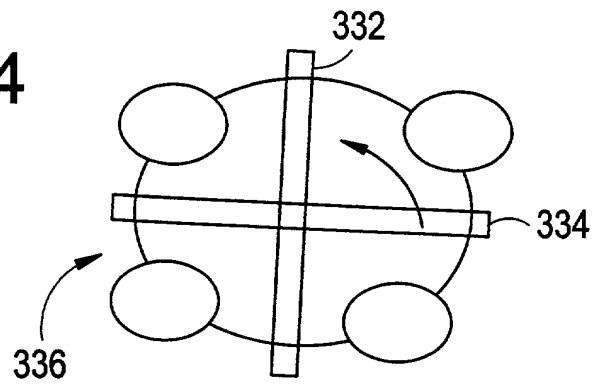


FIG. 35

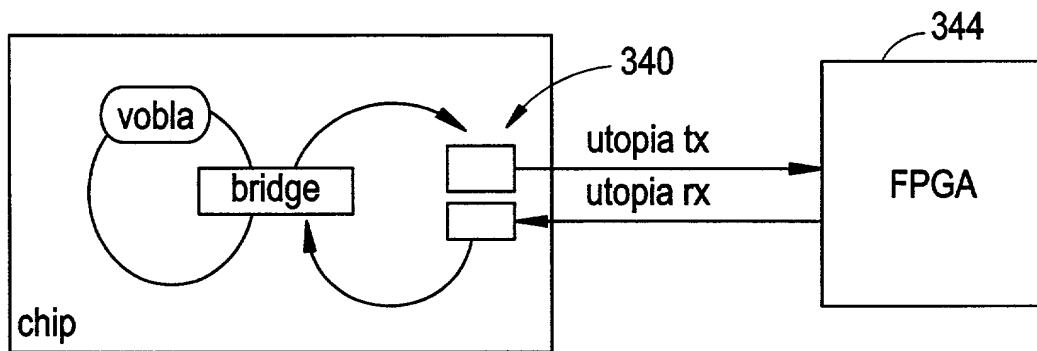


FIG. 36

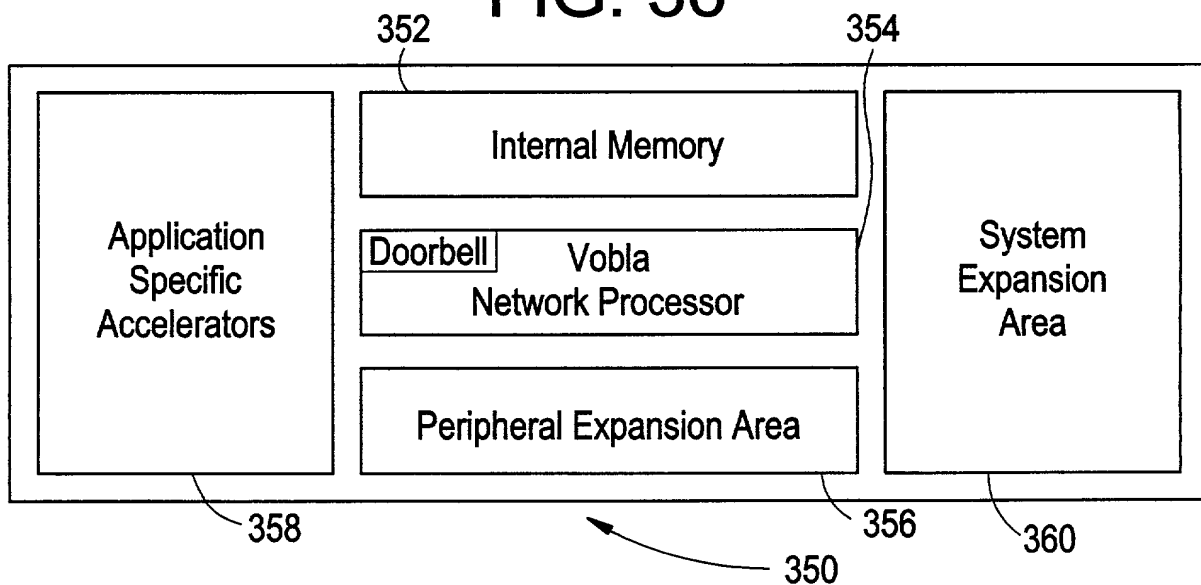
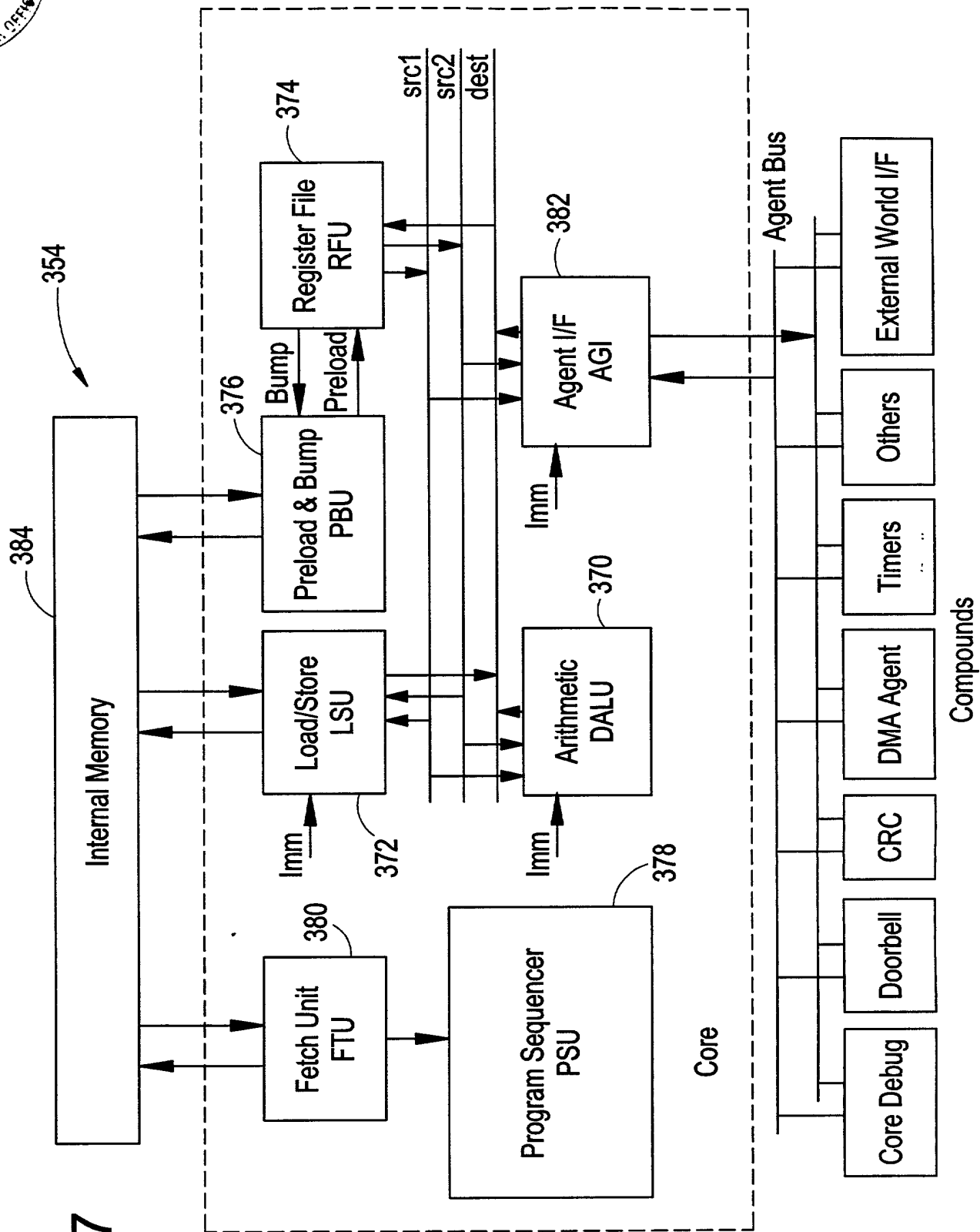
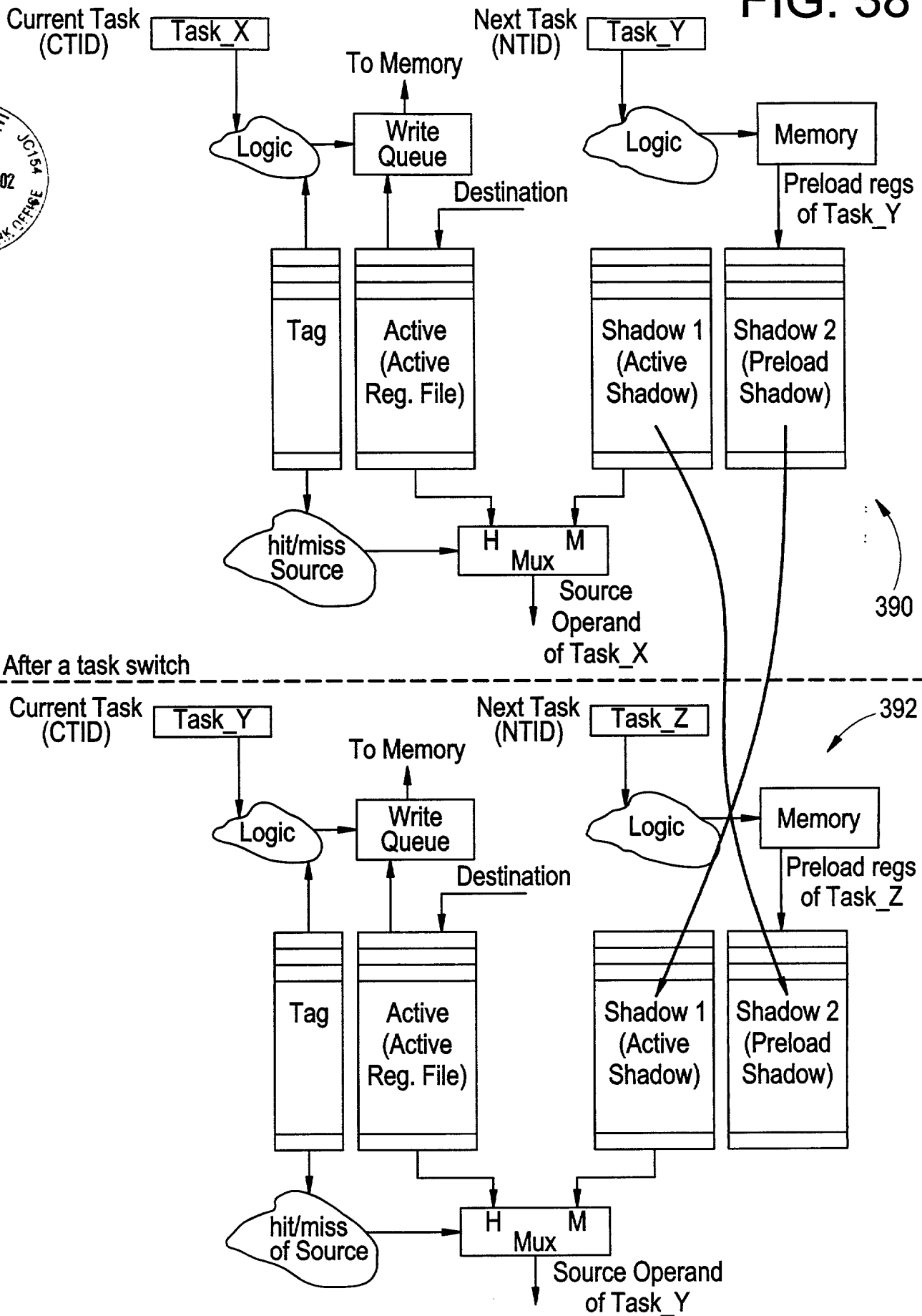


FIG. 37





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FIG. 39

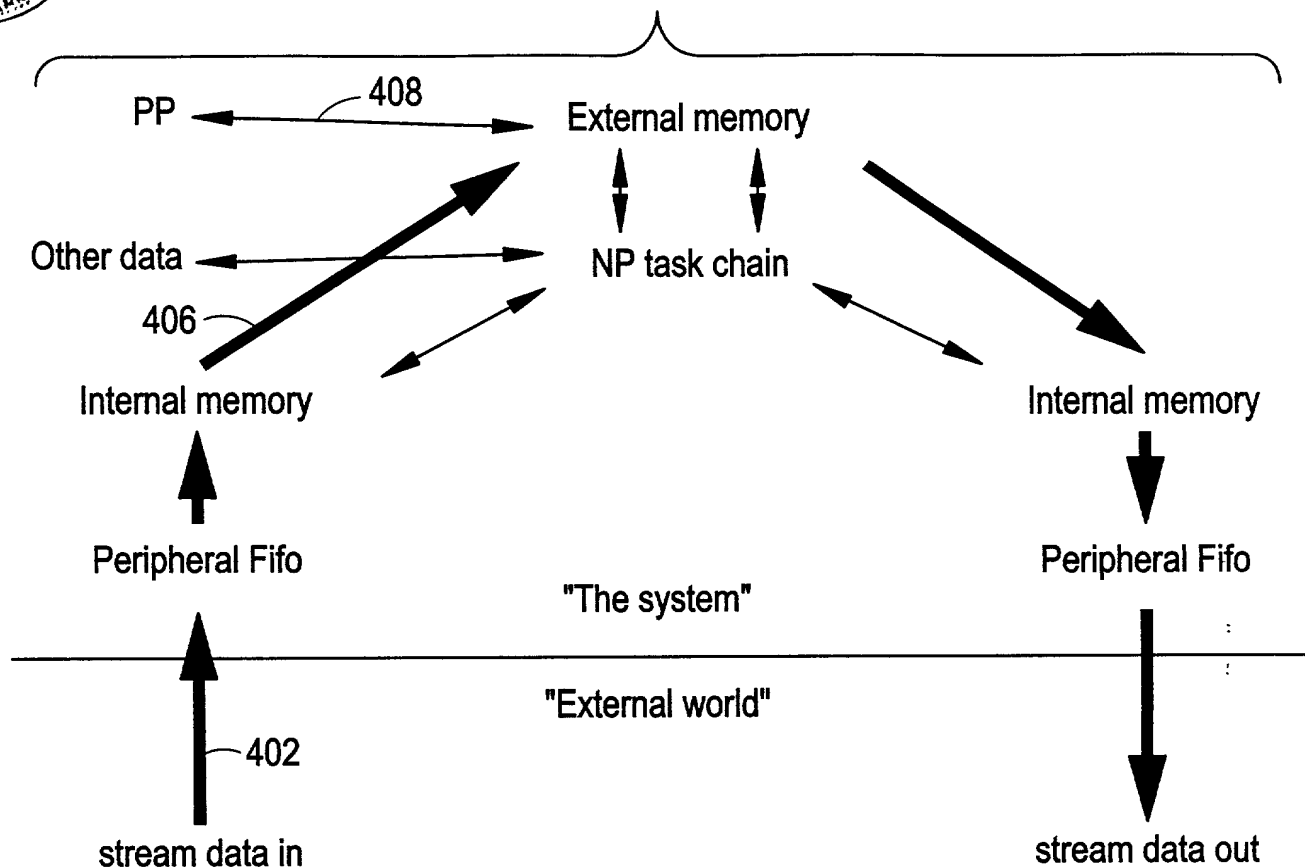


FIG. 40

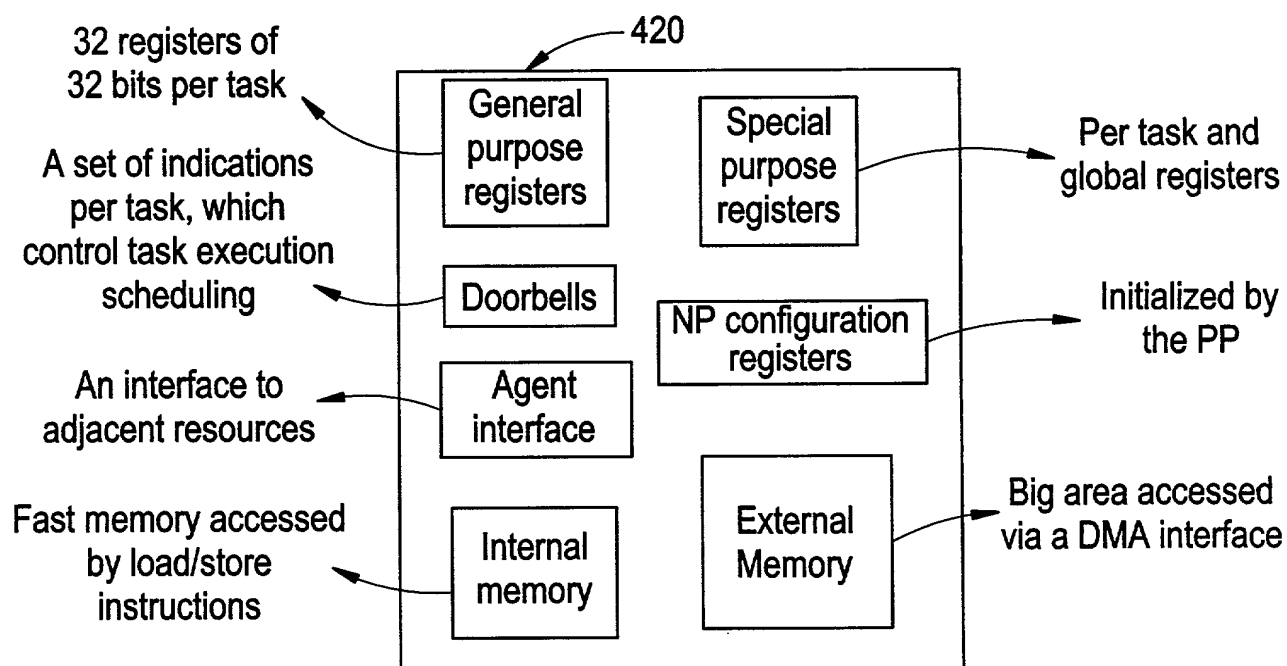


FIG. 41

R1 register

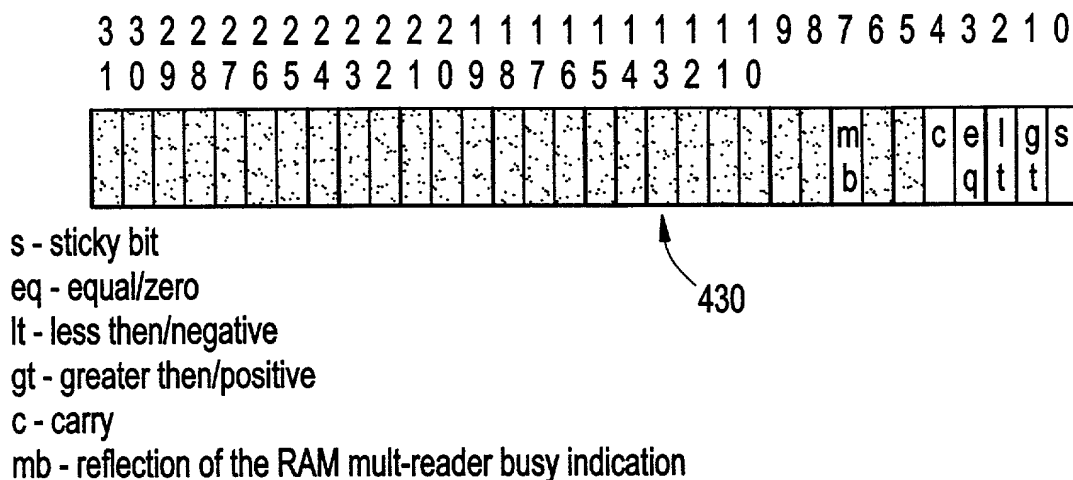


FIG. 42

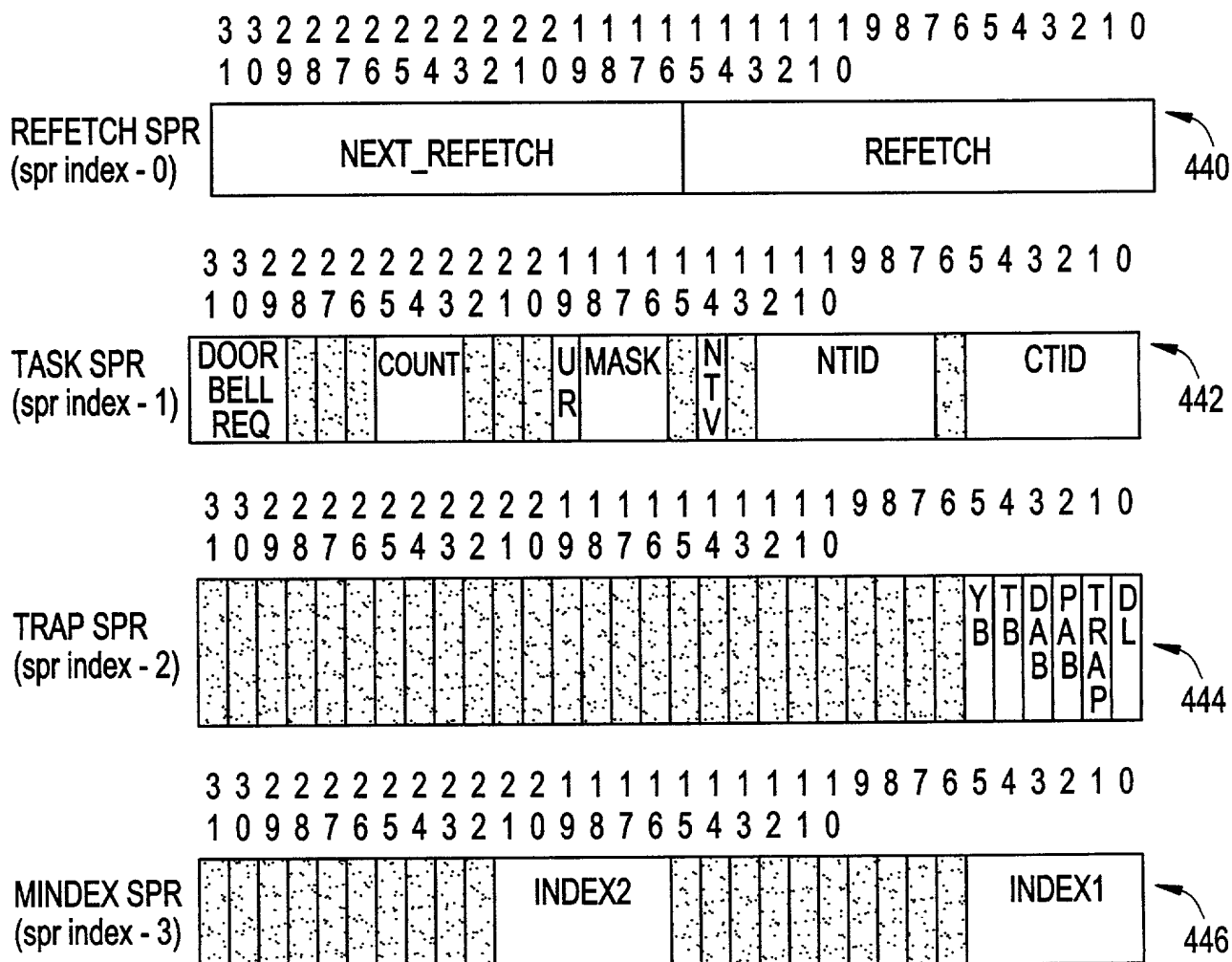




FIG. 43

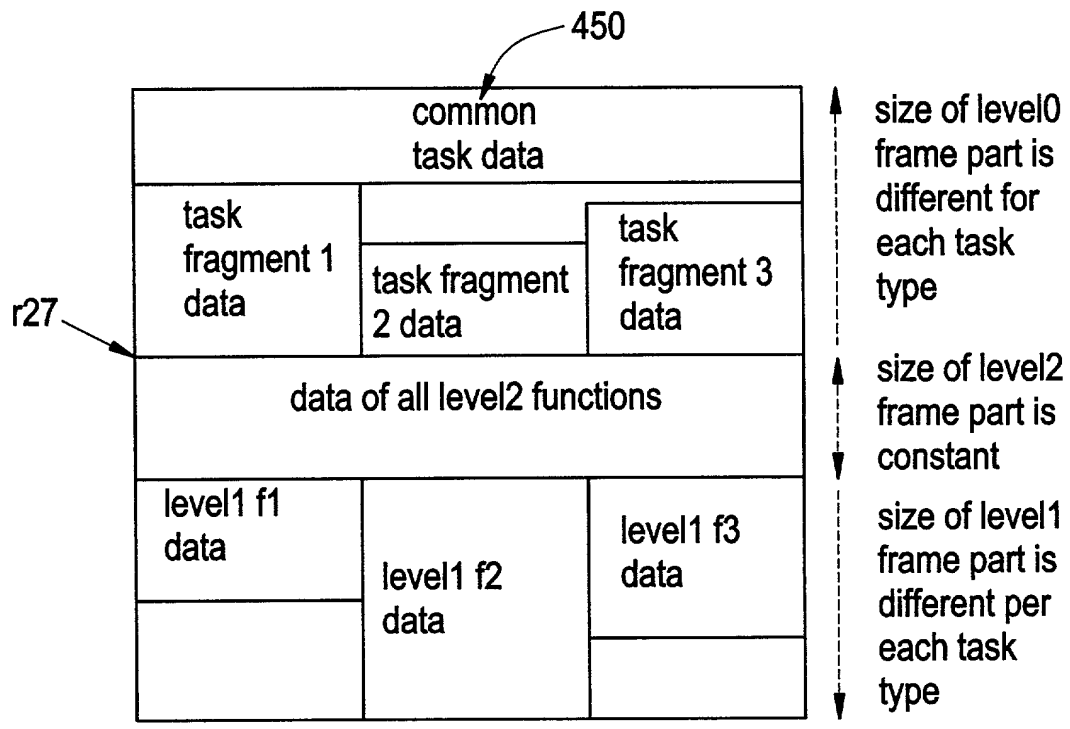


FIG. 44

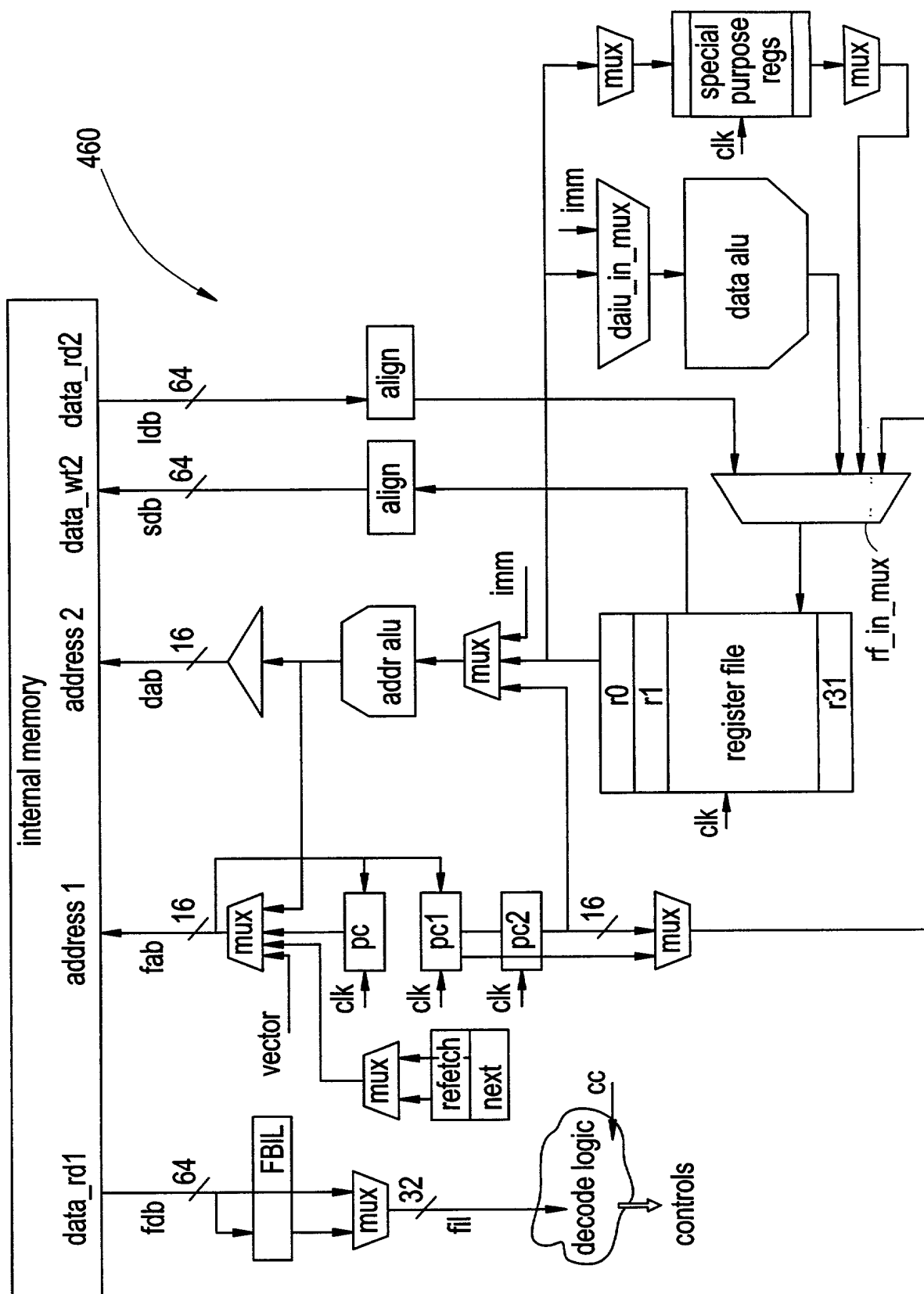
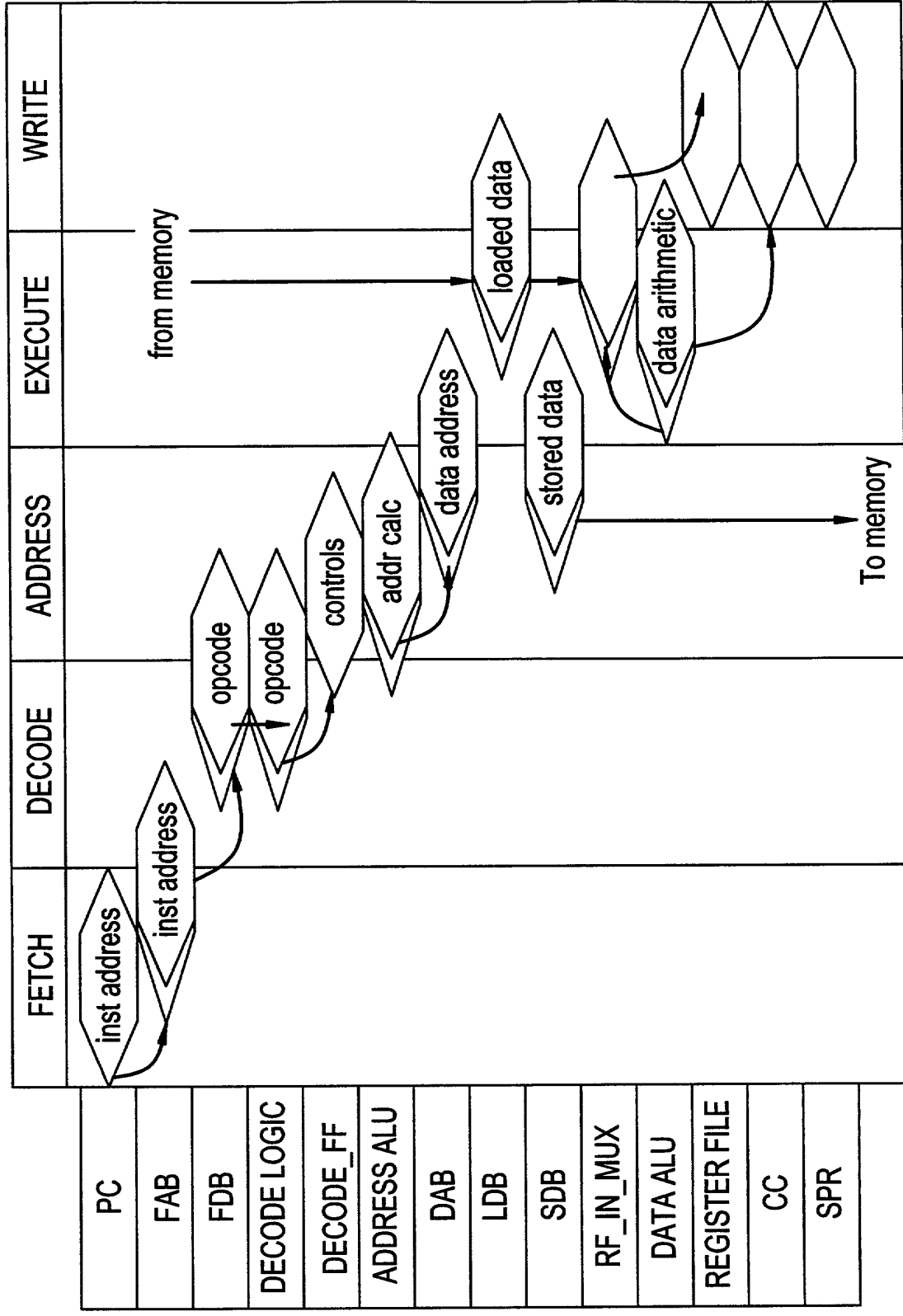


FIG. 45



480

 - Flip Flop
  - Logic

FIG. 46

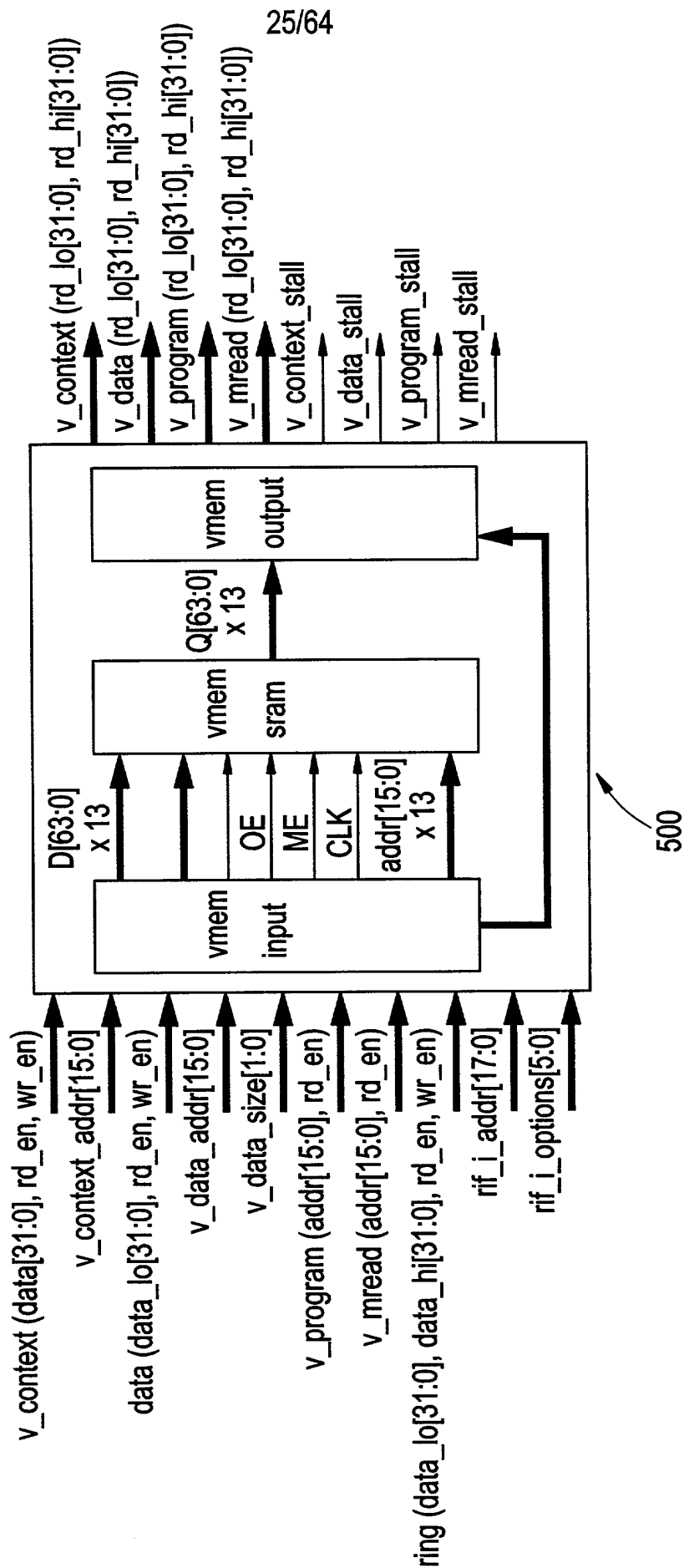


FIG. 47

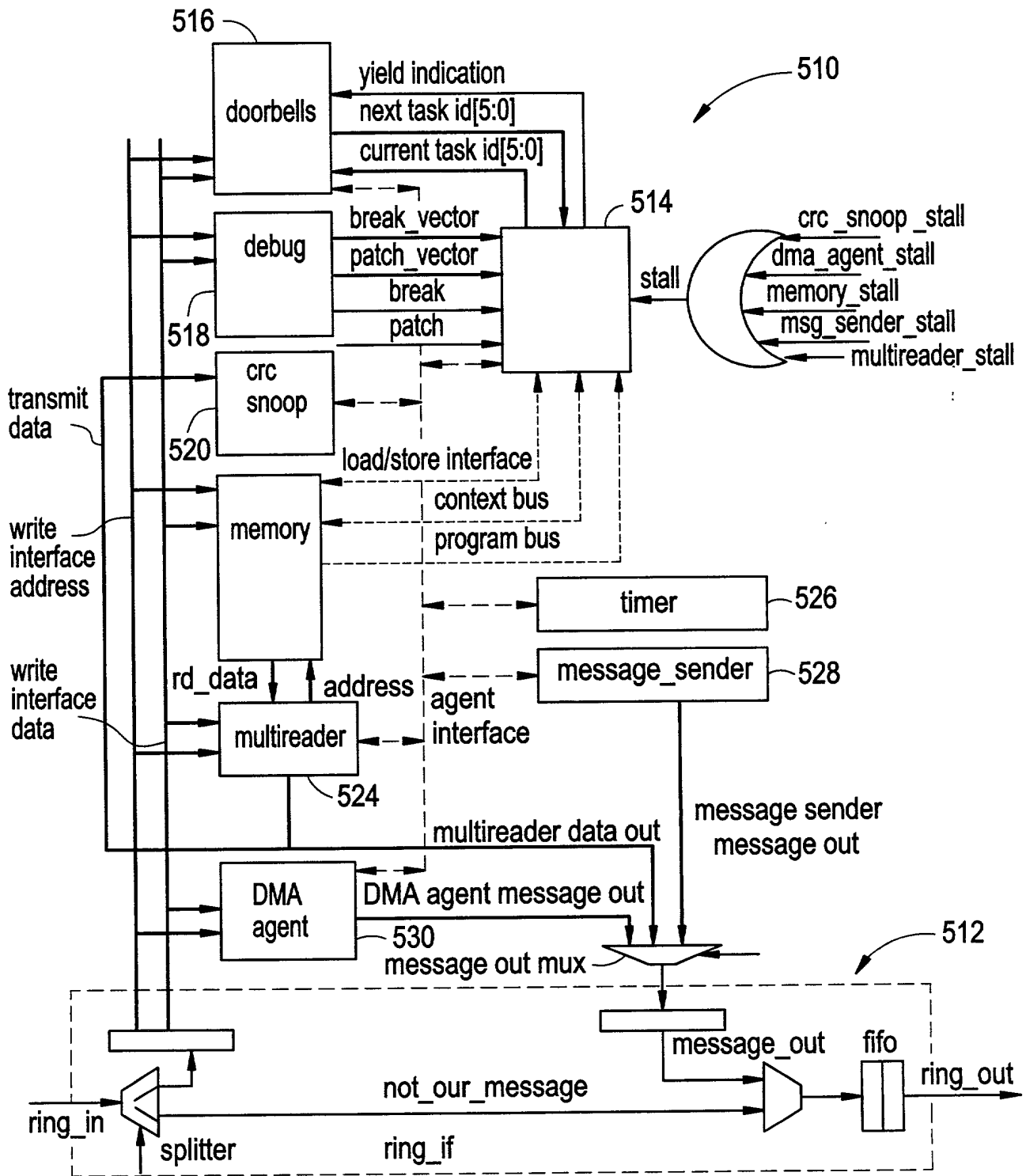


FIG. 48

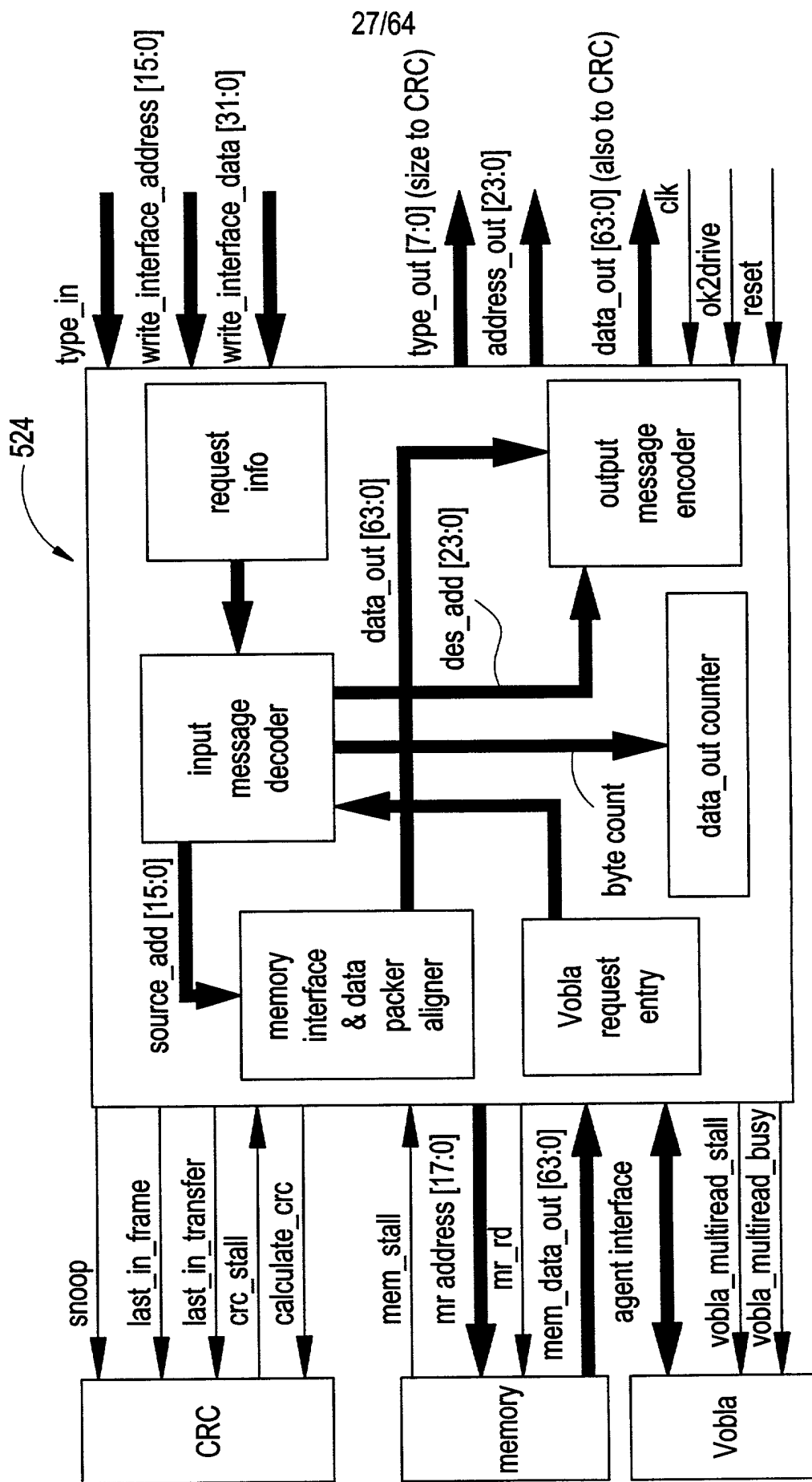




FIG. 49

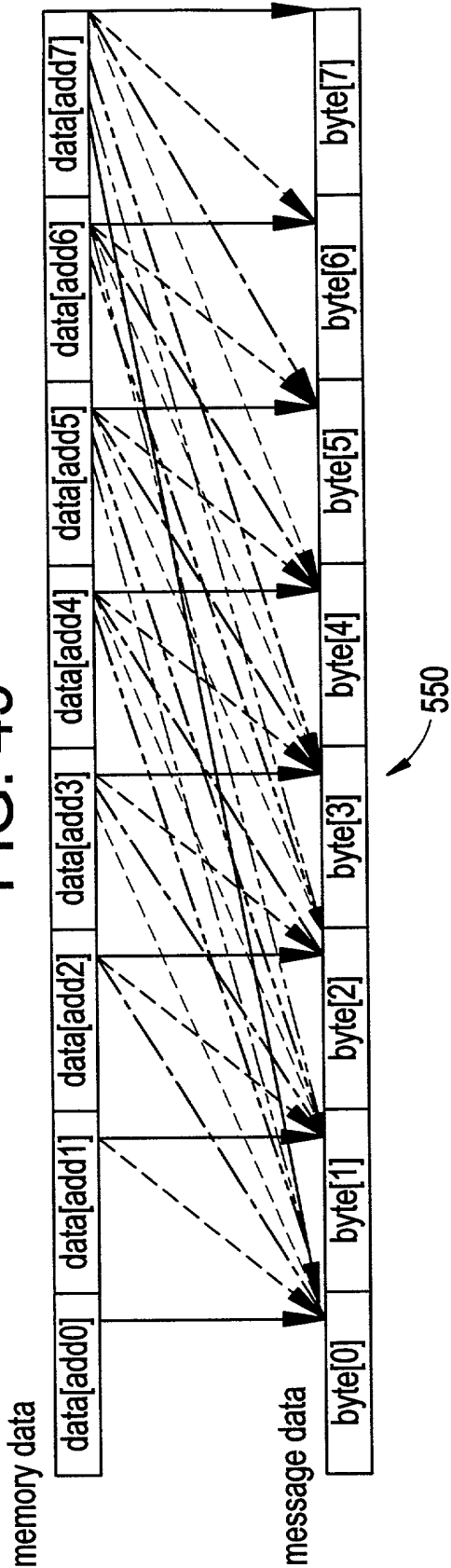


FIG. 50

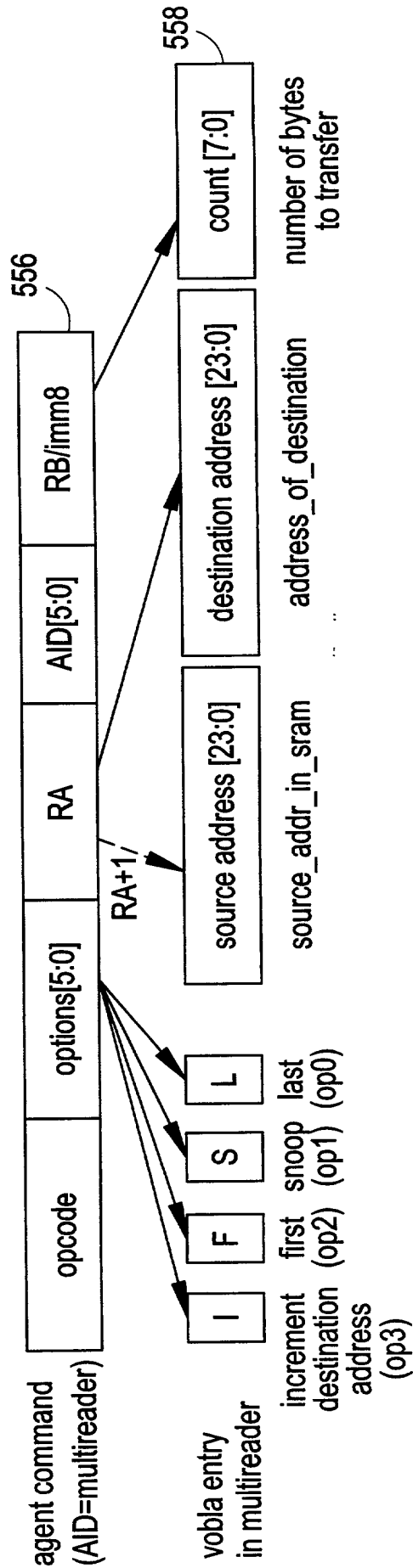


FIG. 51

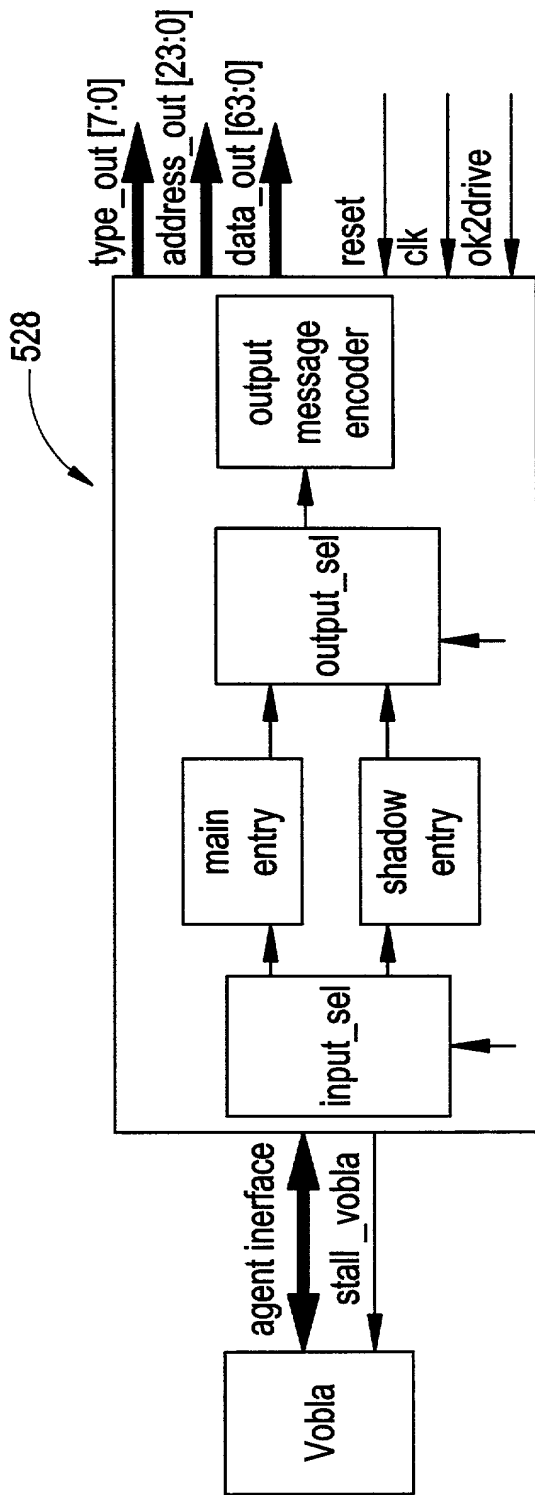
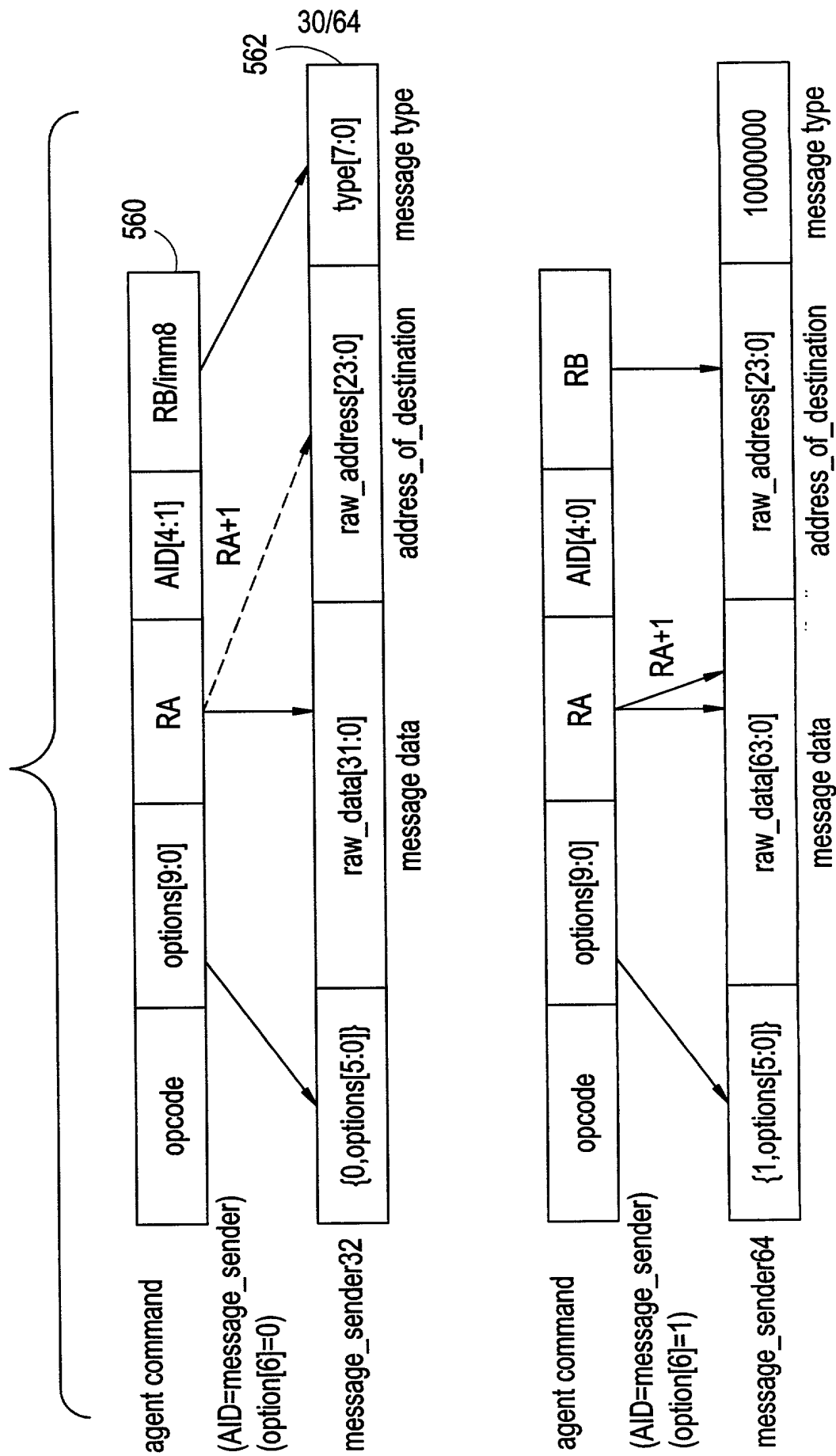




FIG. 52



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FIG. 53

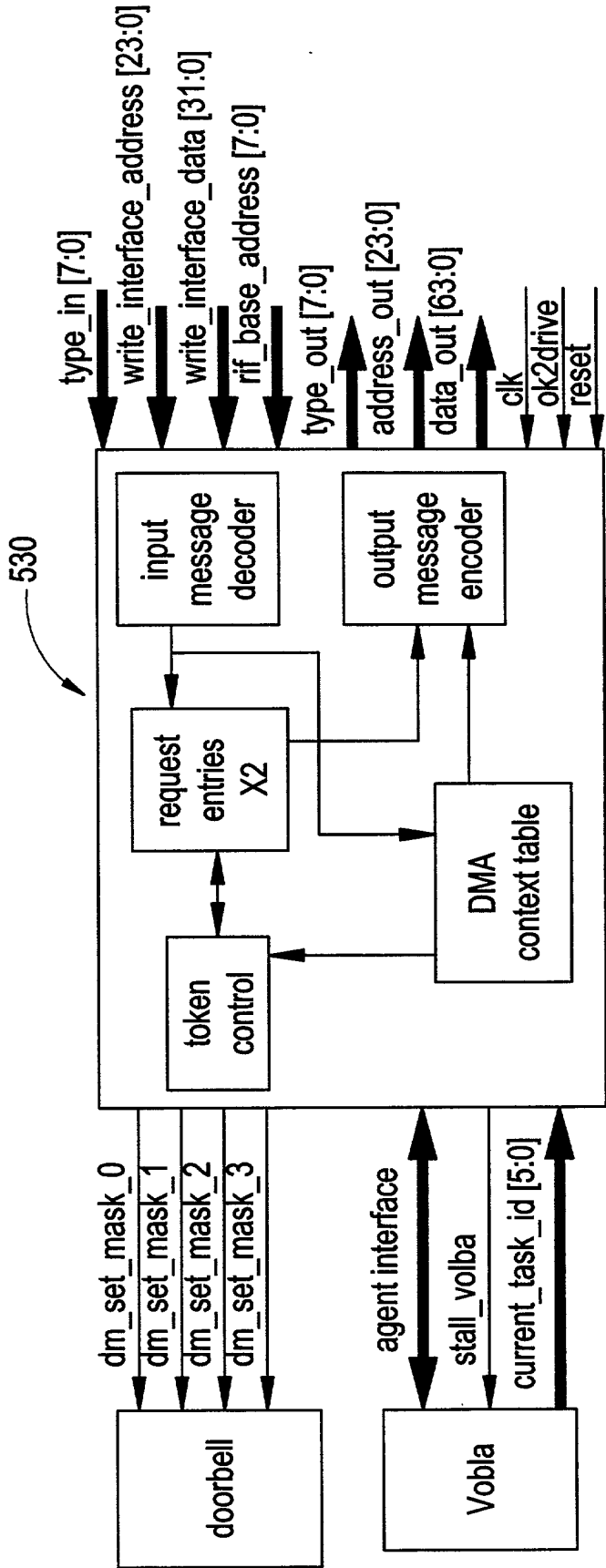


FIG. 54

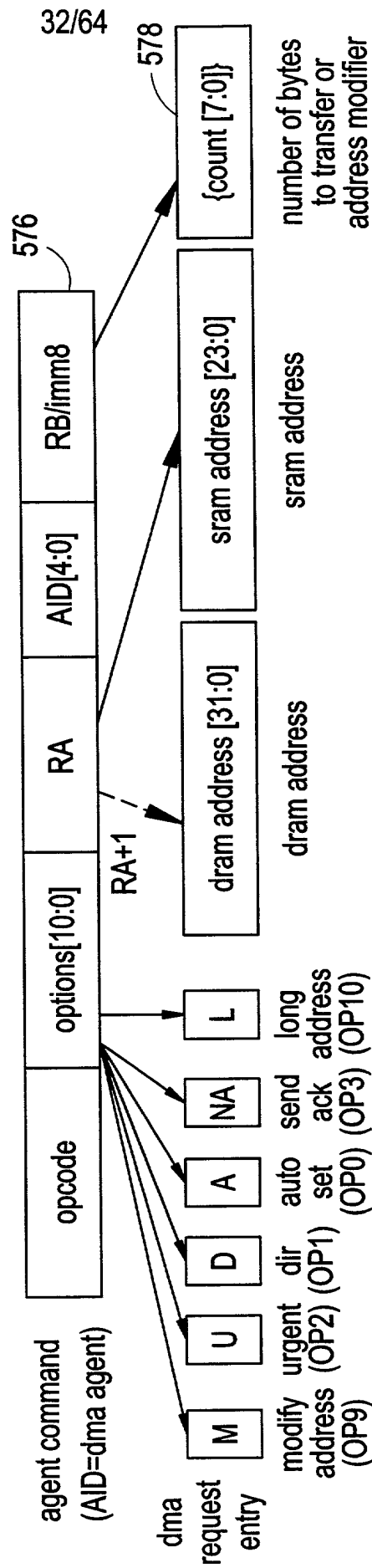


FIG. 55

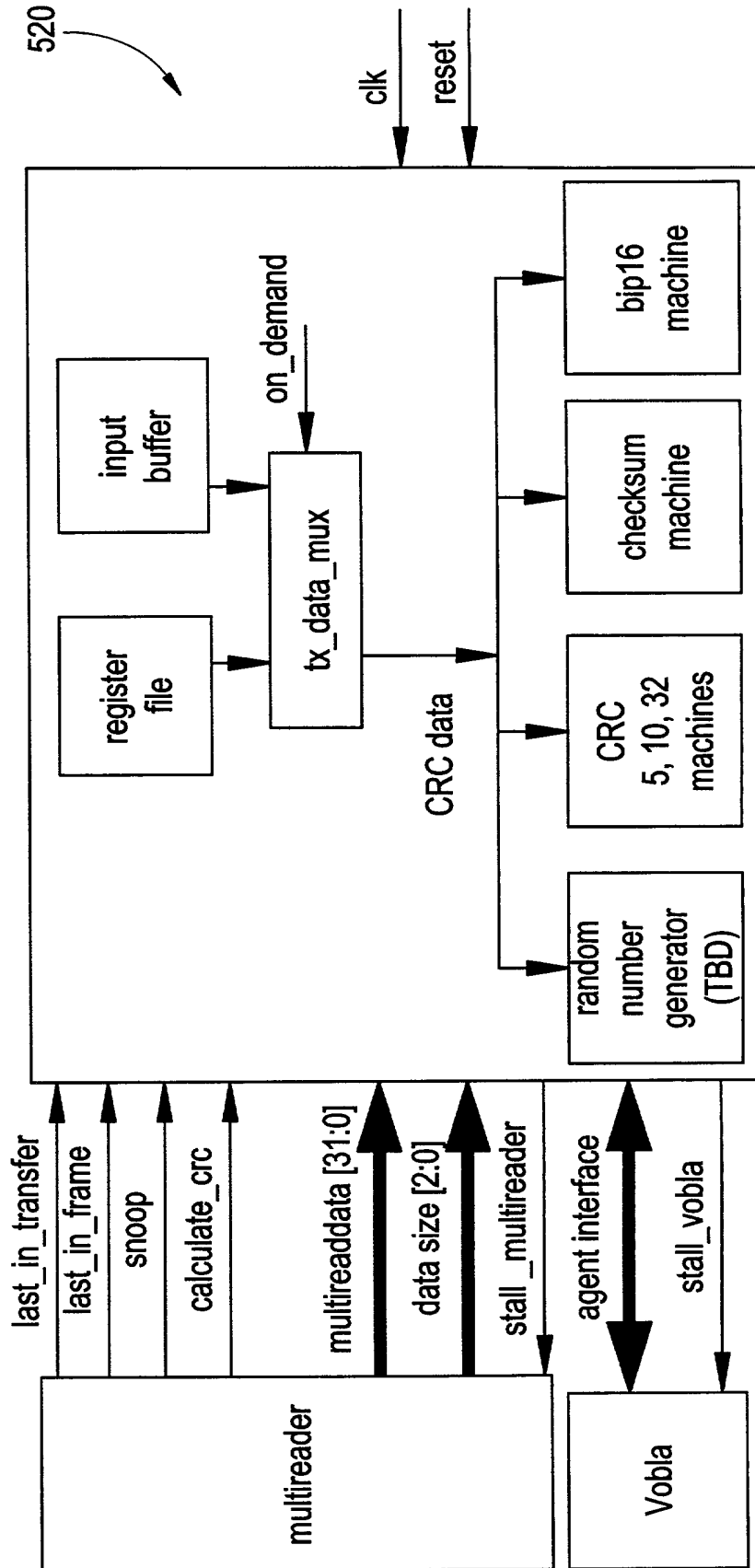


FIG. 56

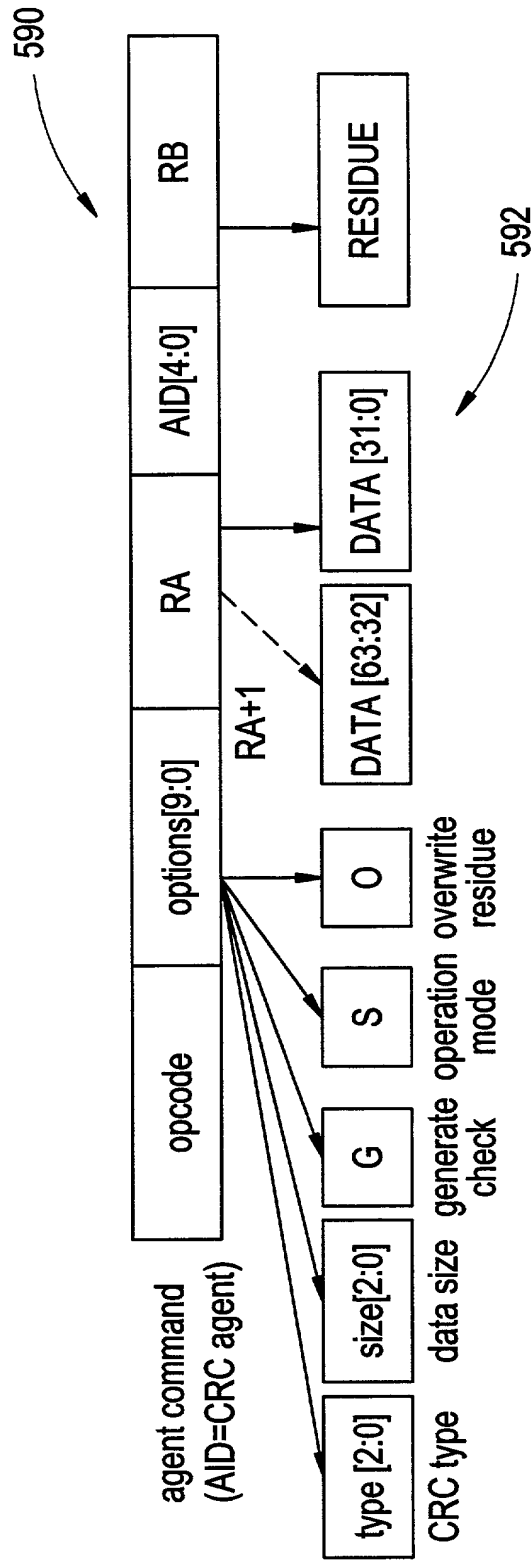


FIG. 57

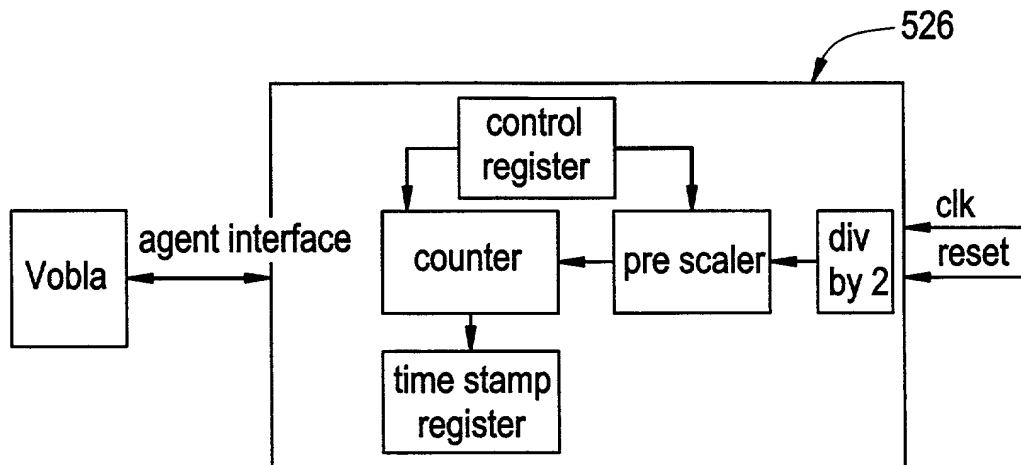


FIG. 58

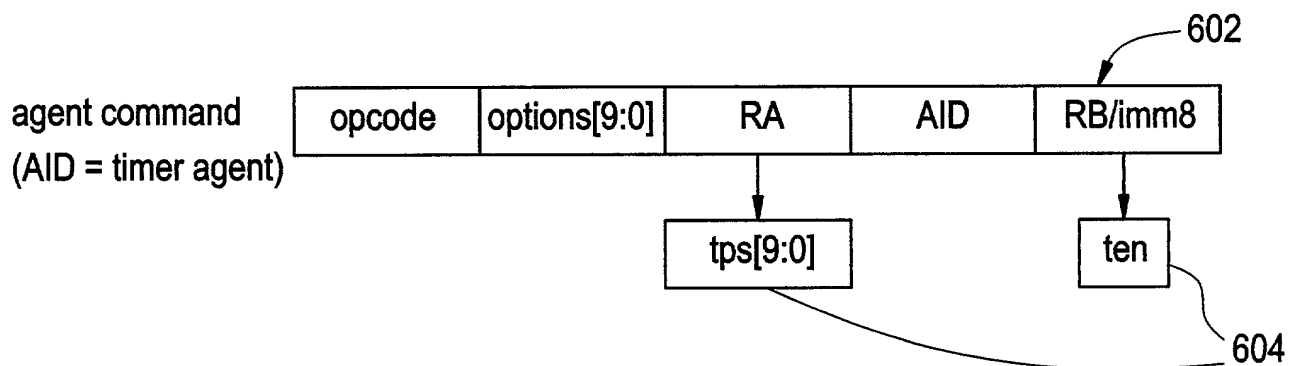


FIG. 61

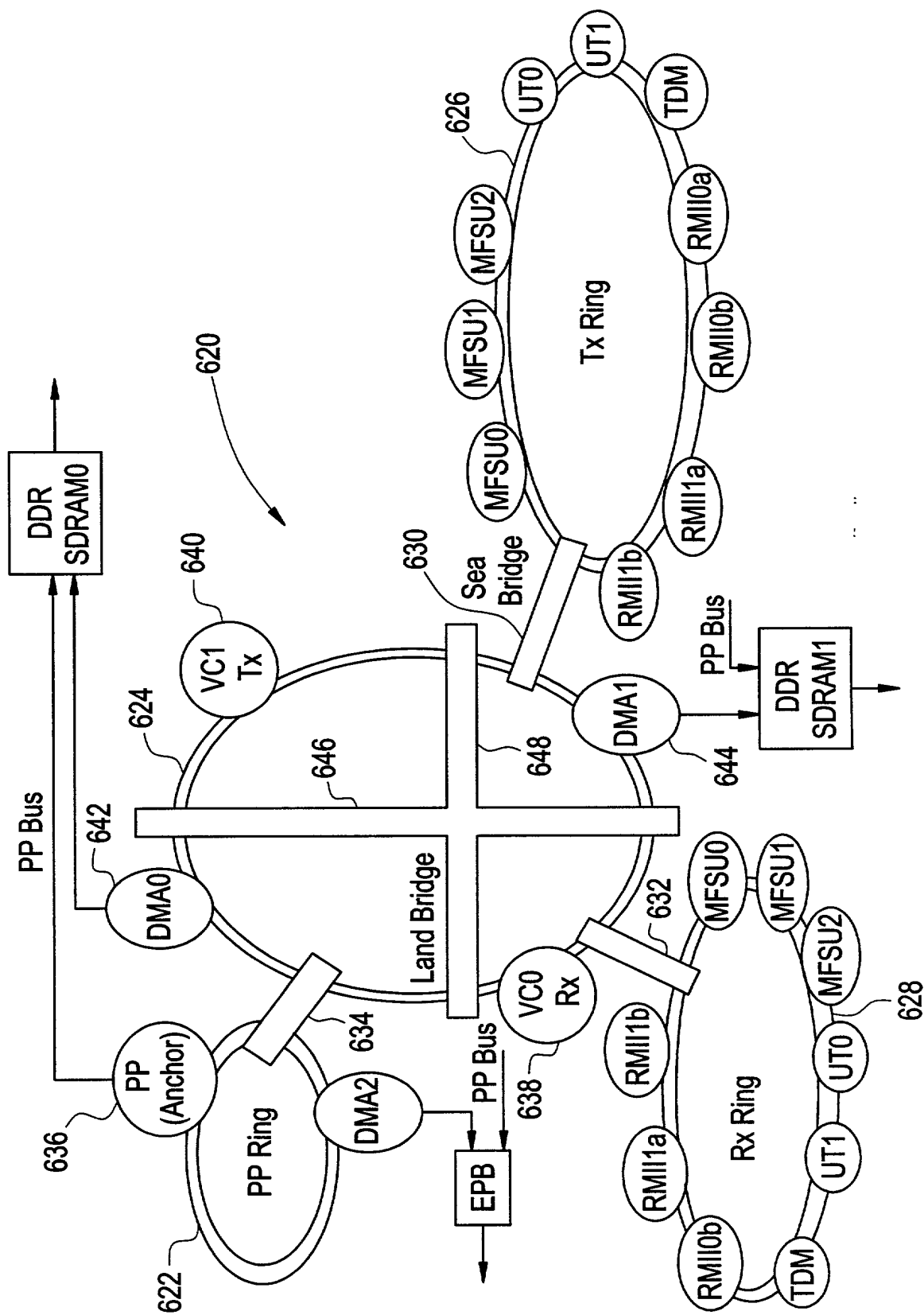


FIG. 62

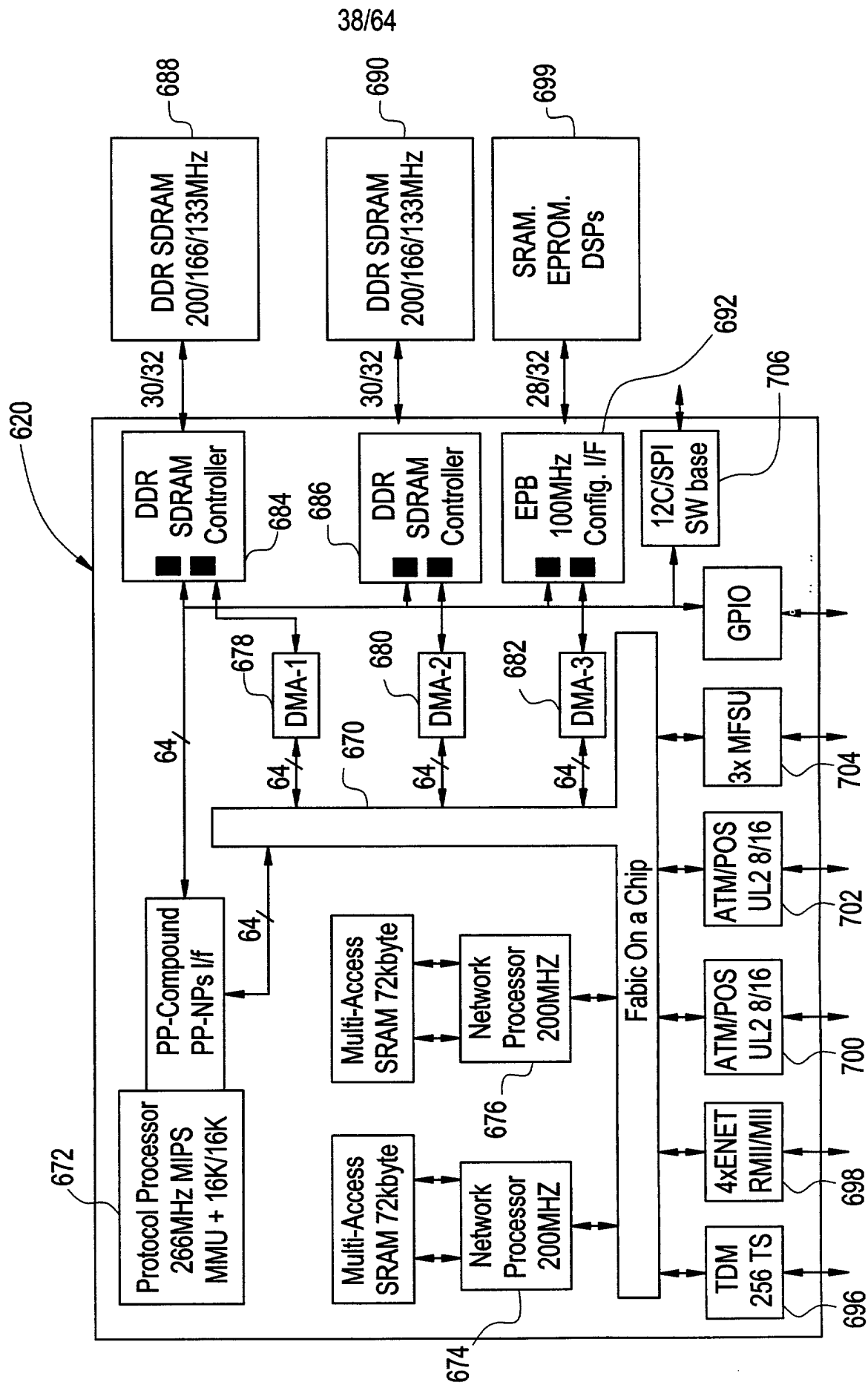


FIG. 63

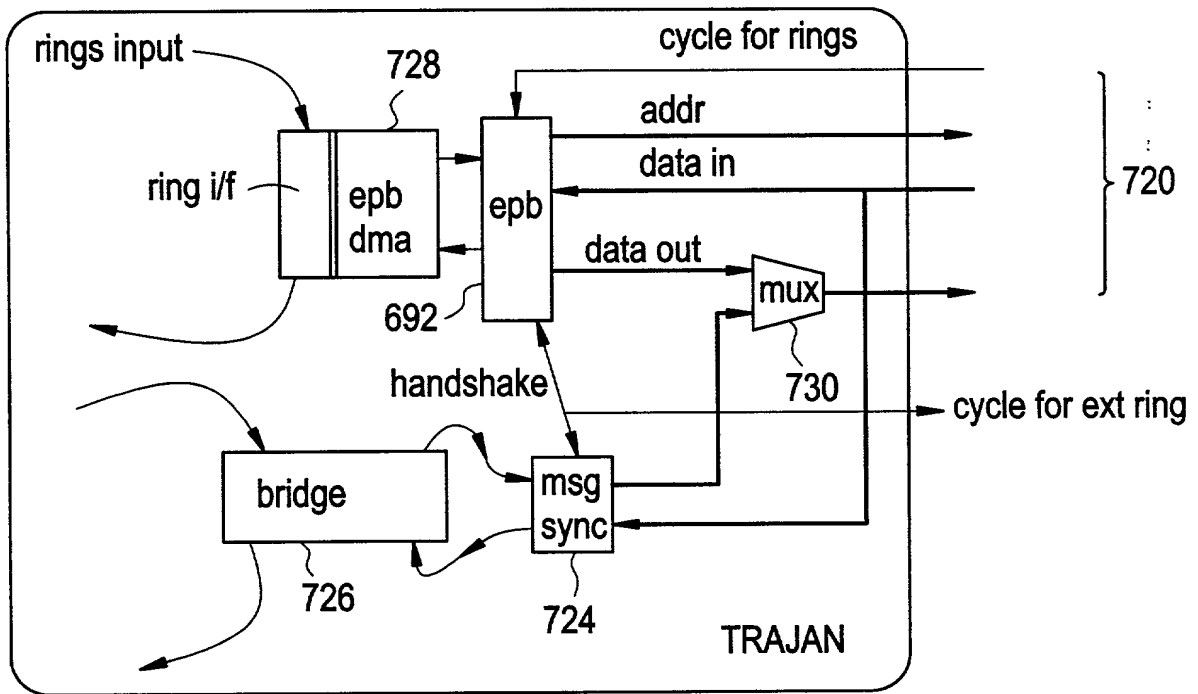


FIG. 64

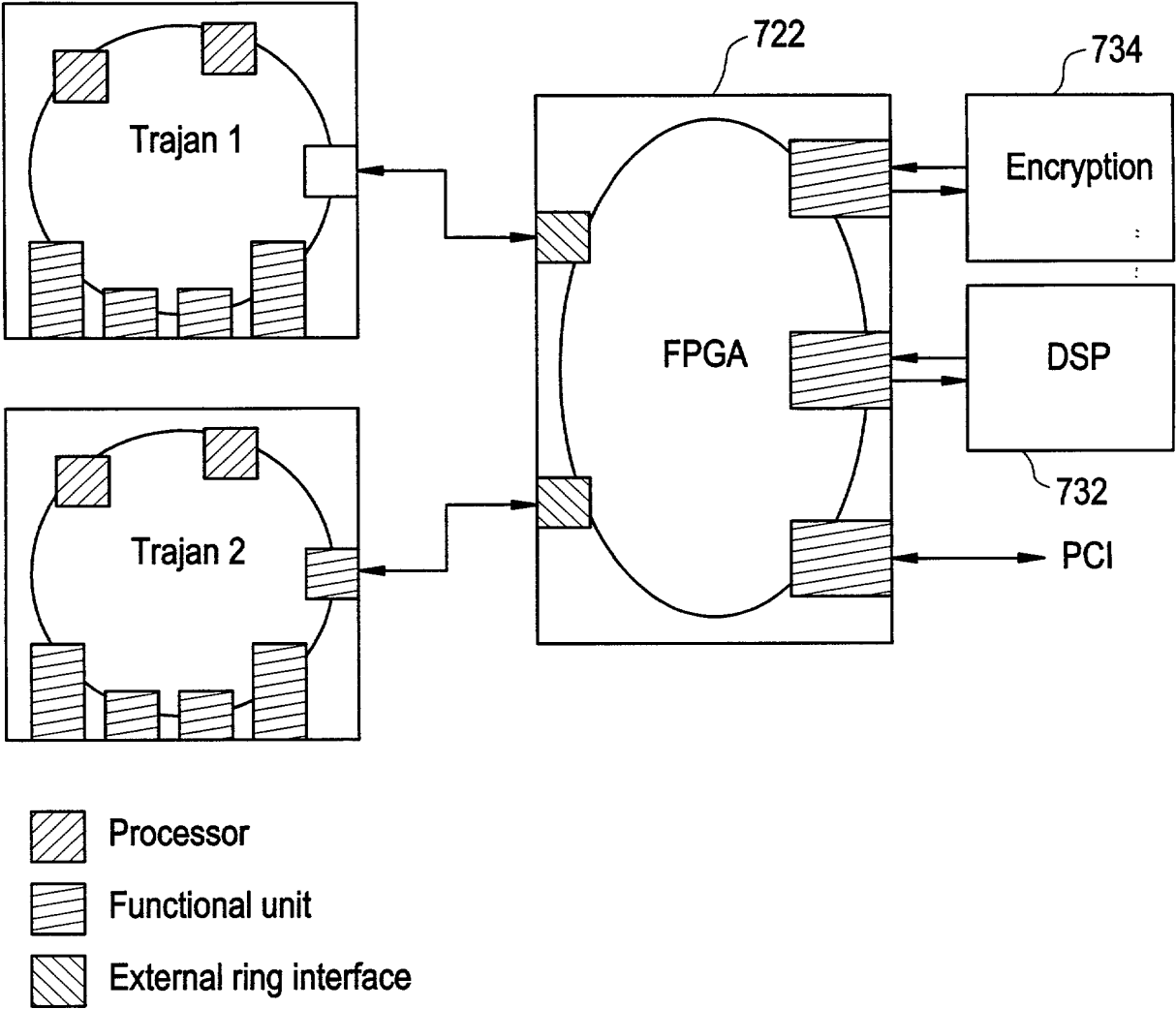


FIG. 65

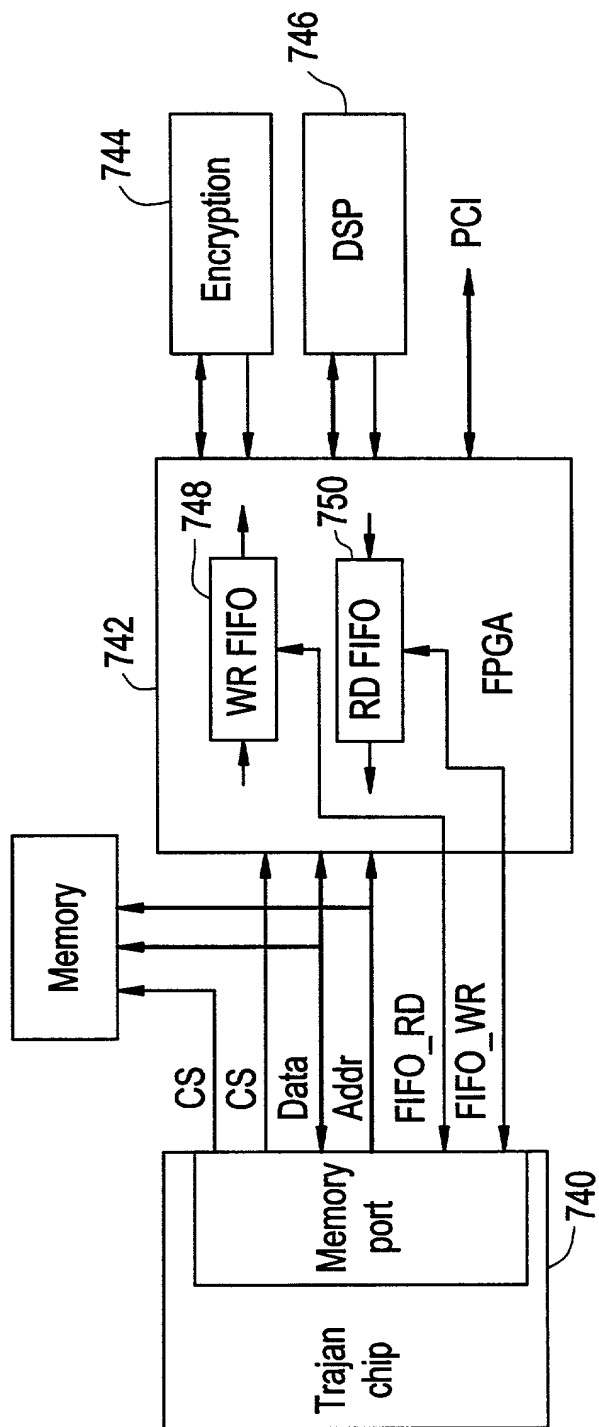


FIG. 66

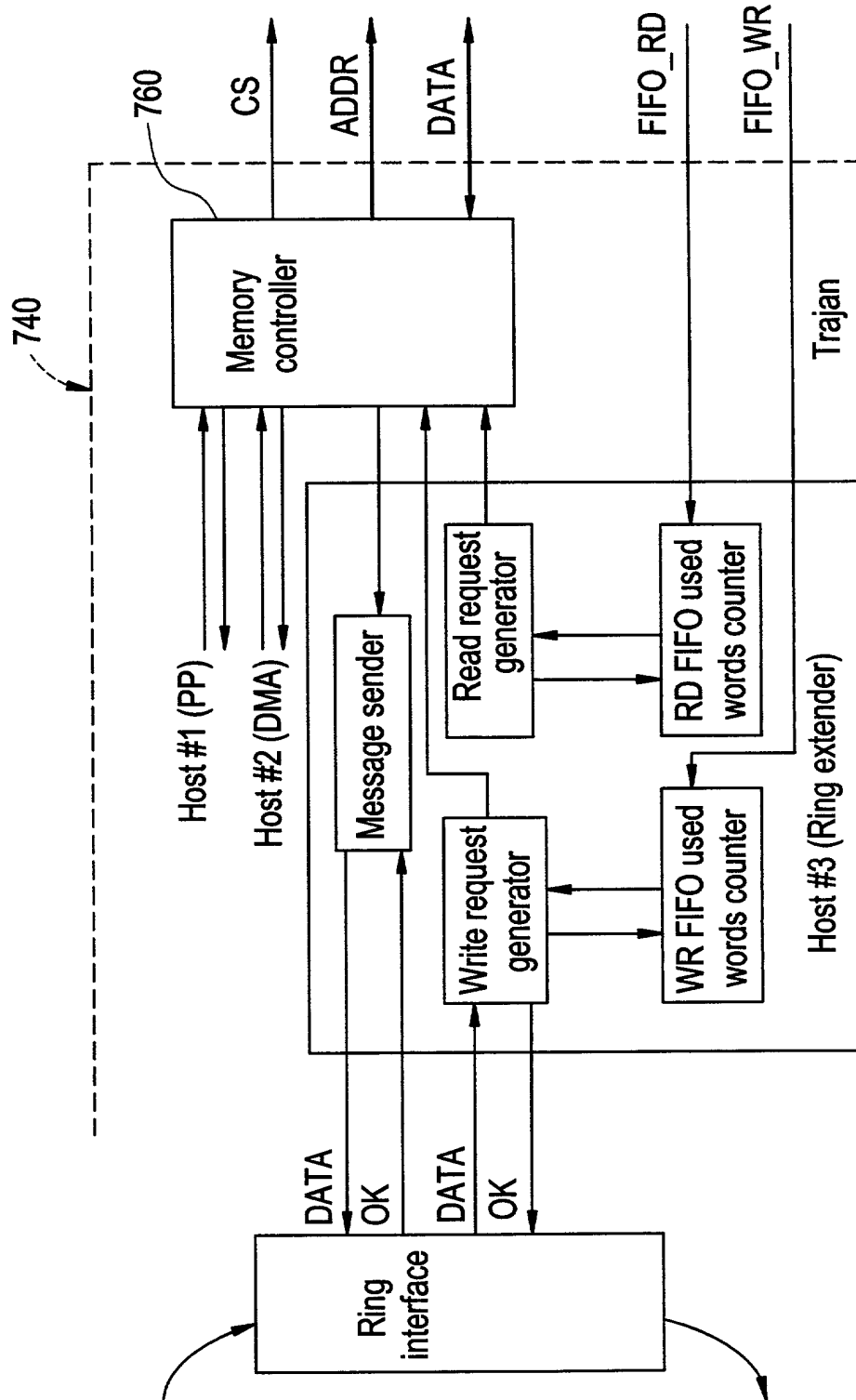
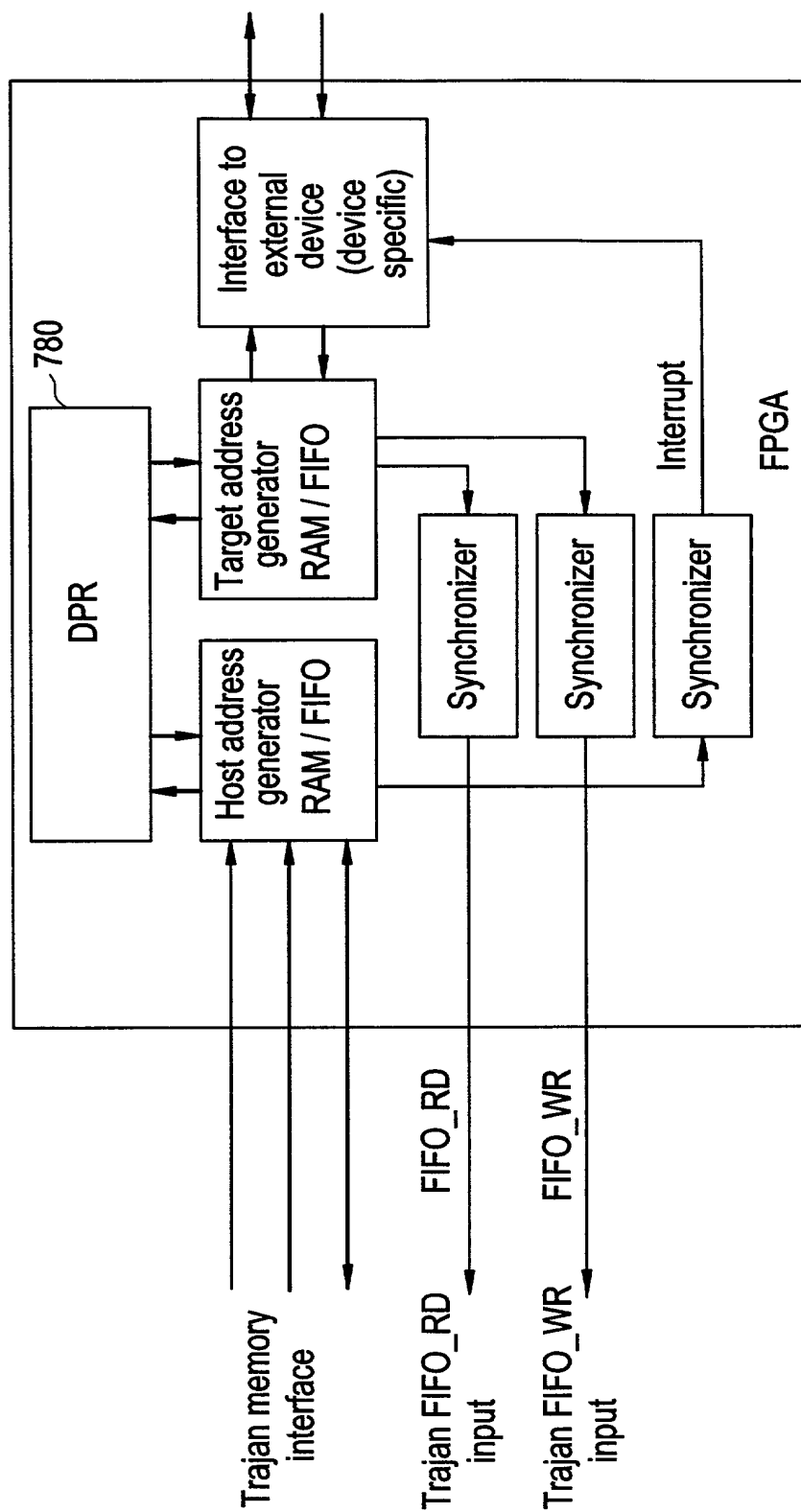


FIG. 67



209260" Tech900T

FIG. 68

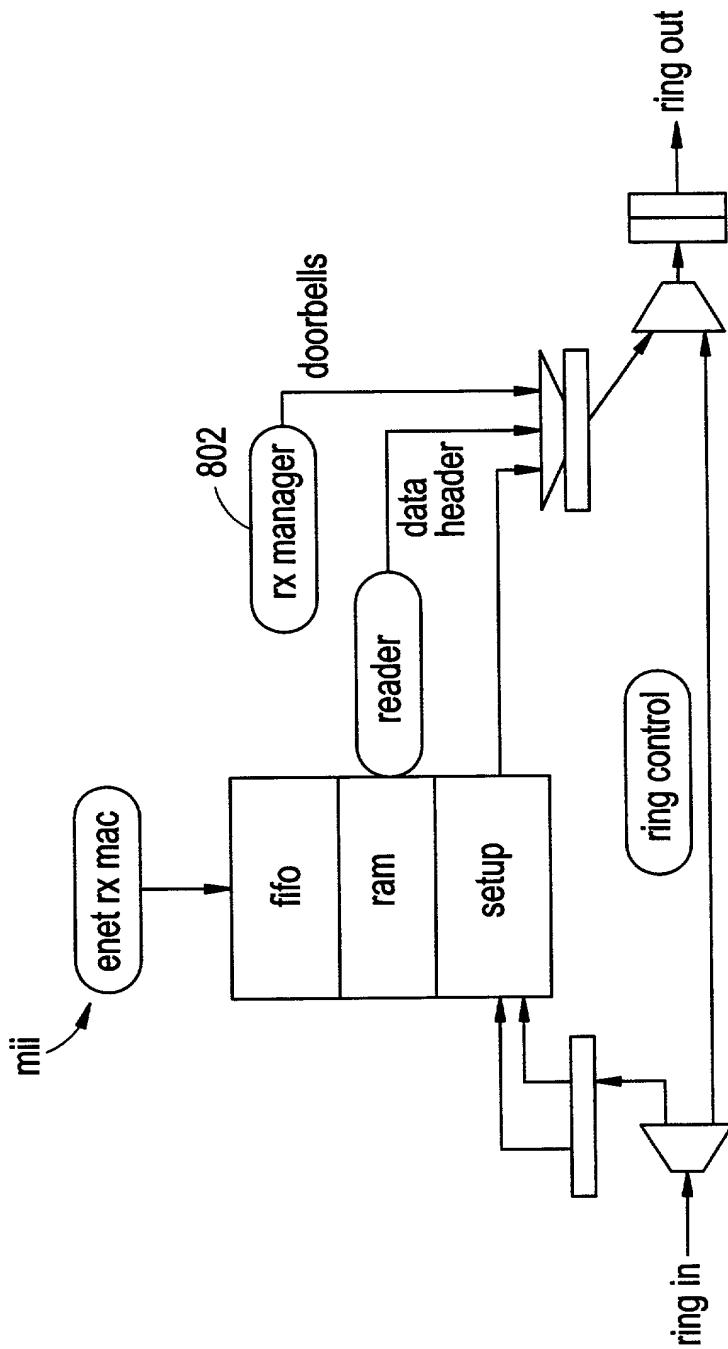


FIG. 69

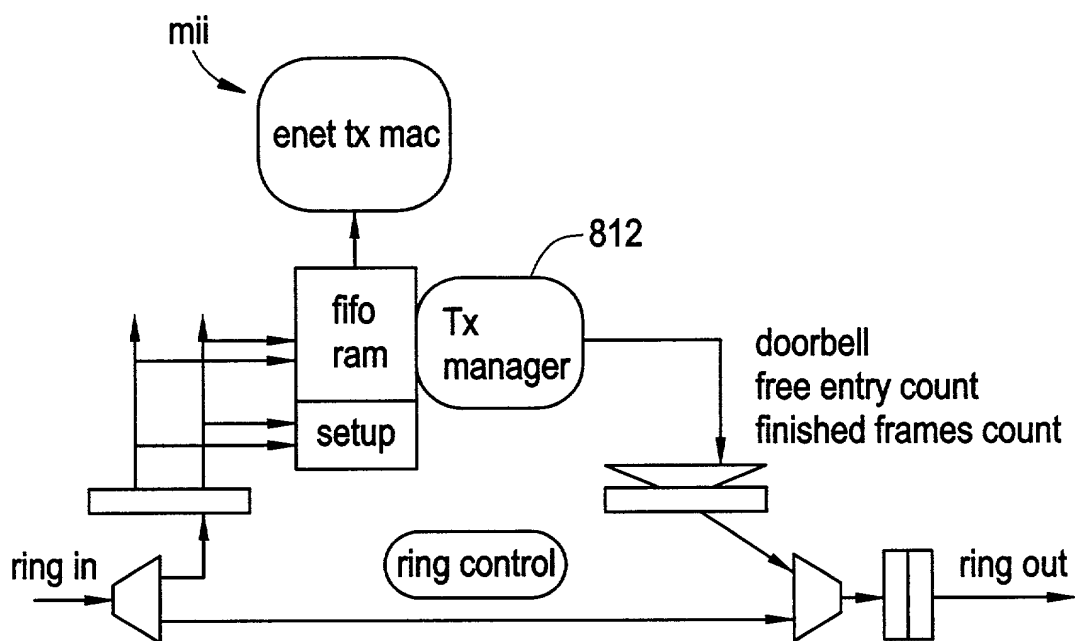
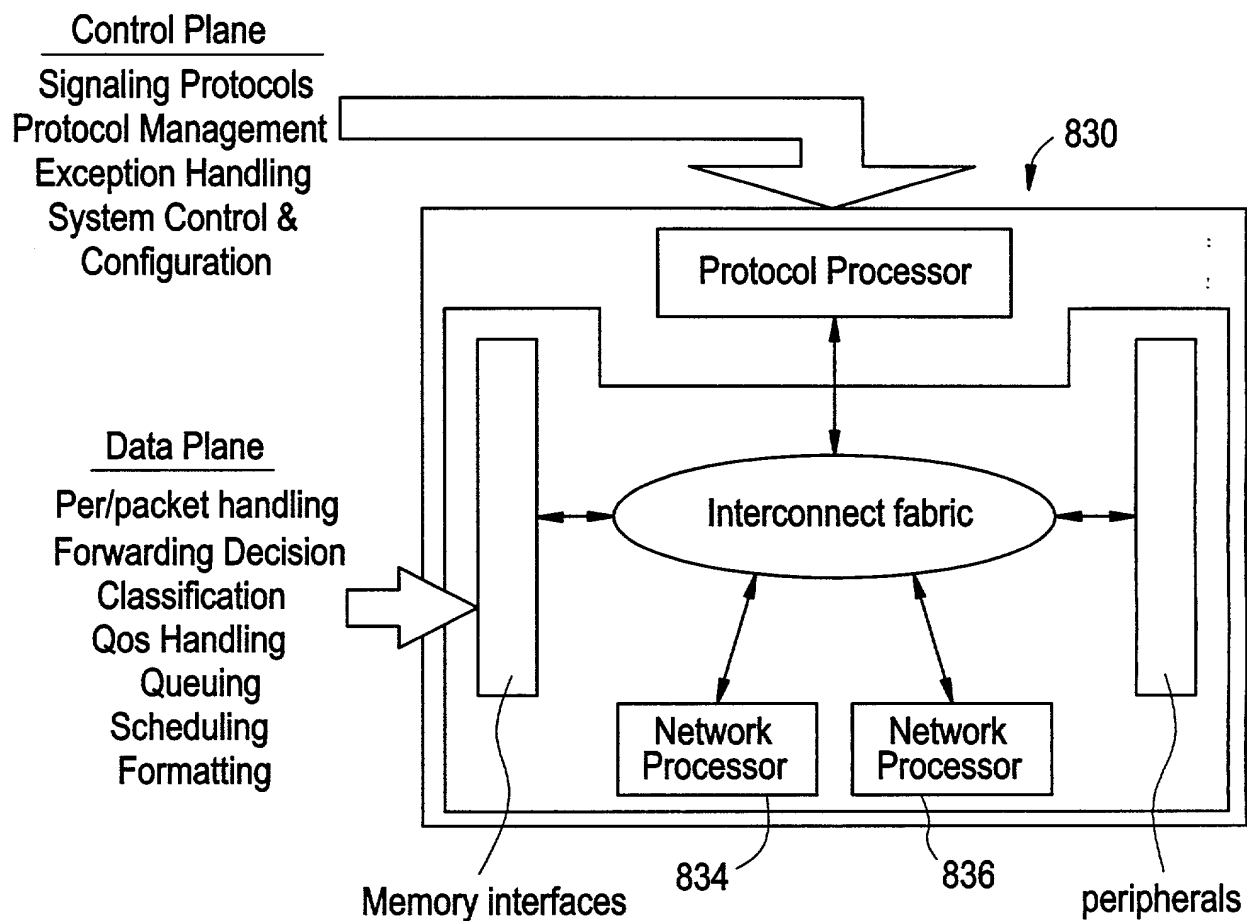


FIG. 70



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FIG. 71

840

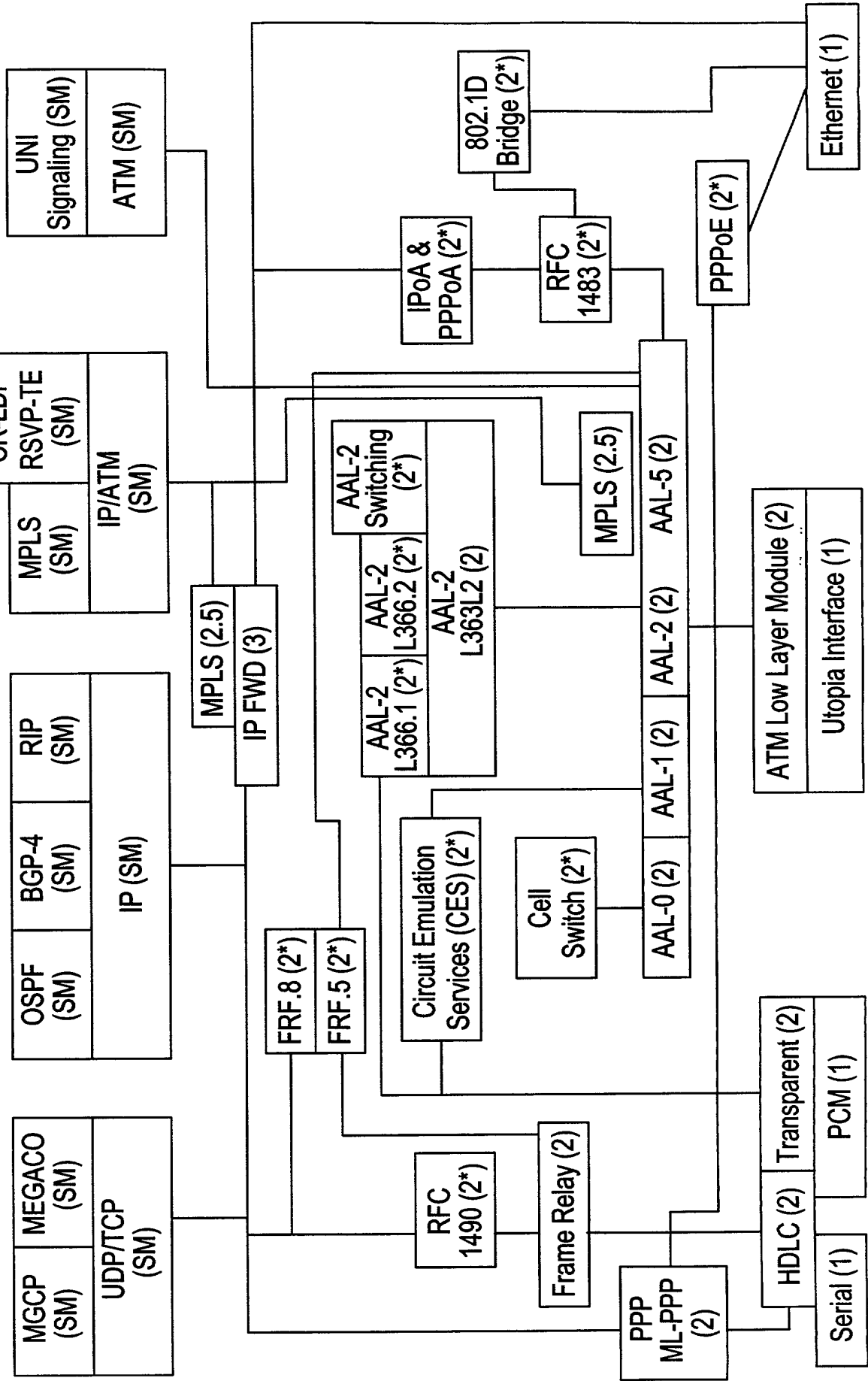




FIG. 72

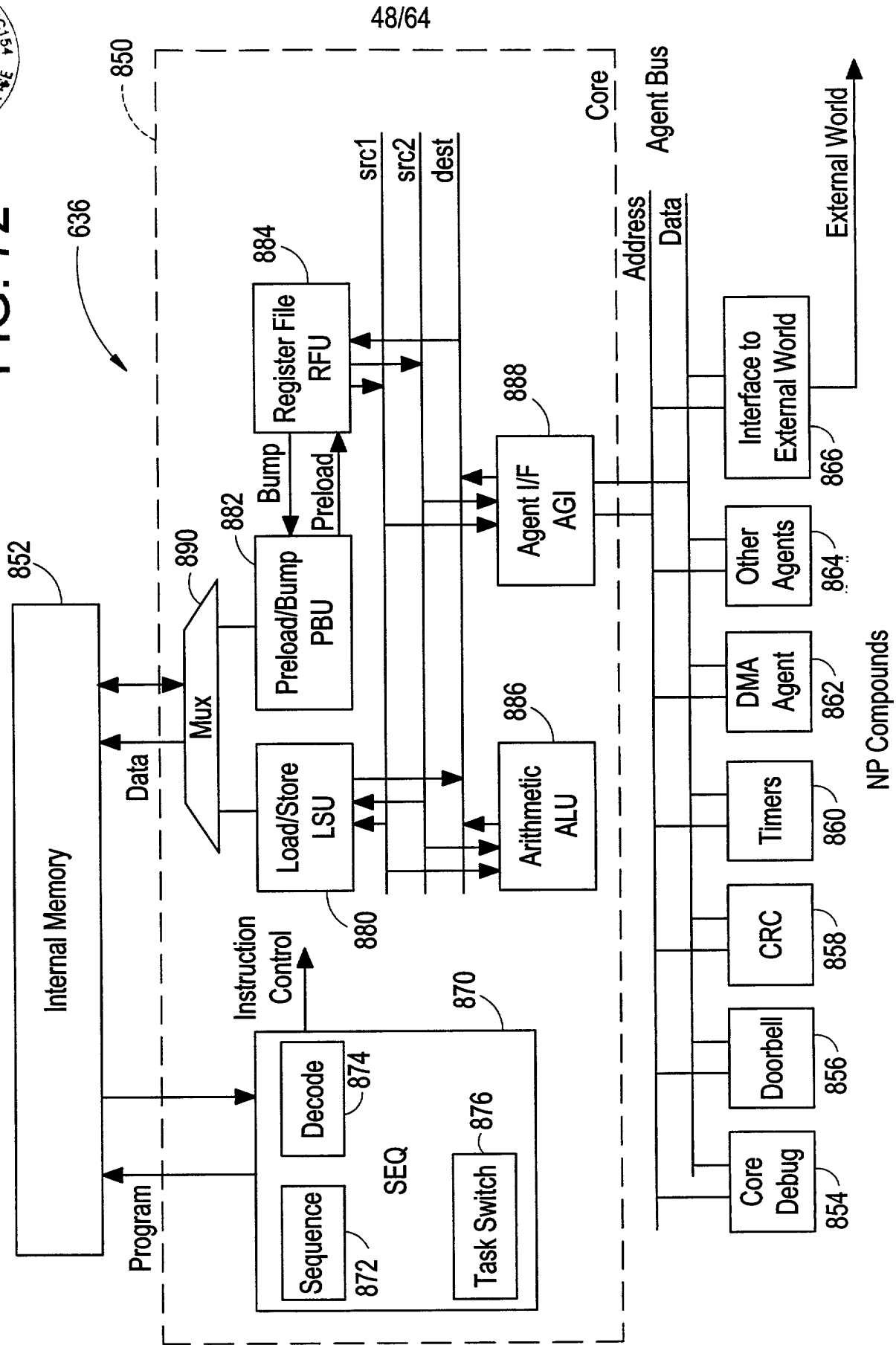
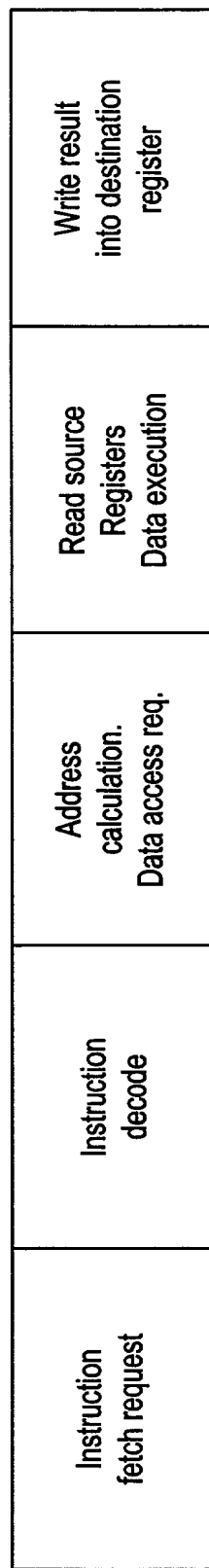
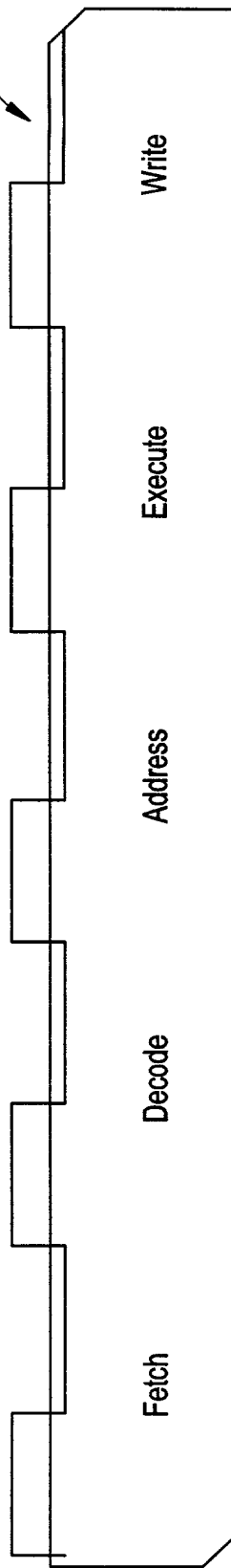


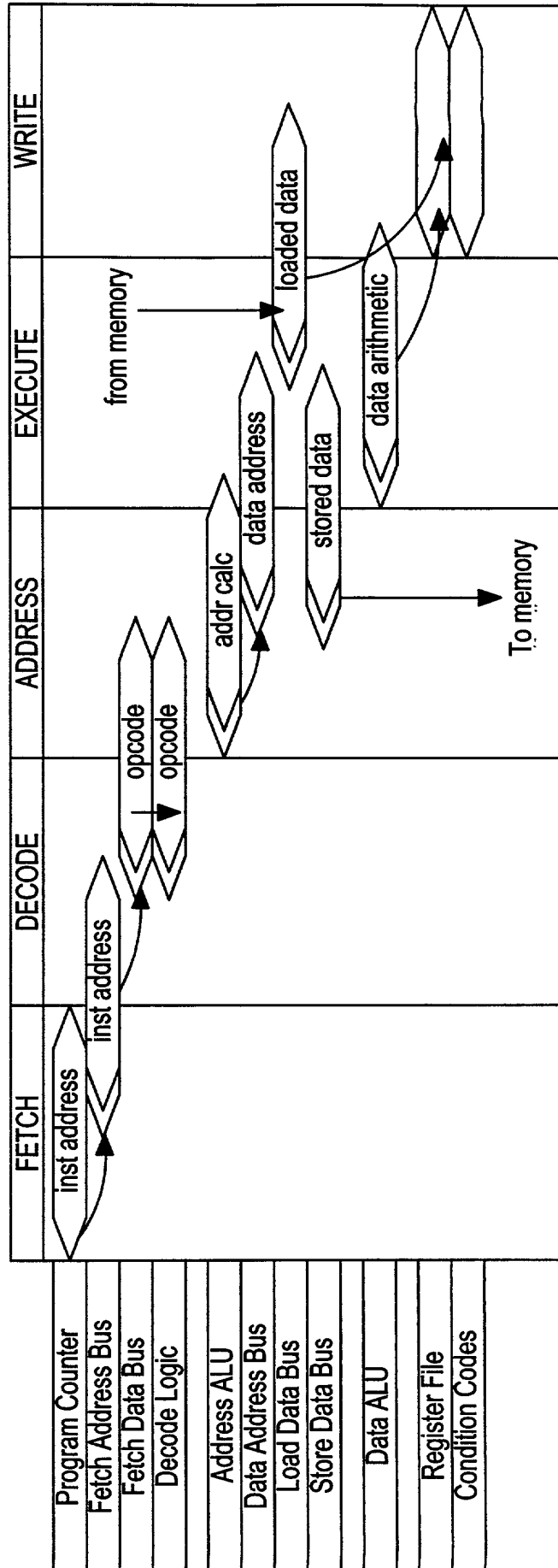


FIG. 73

900



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◊ - Flip Flop ◊ - Logic

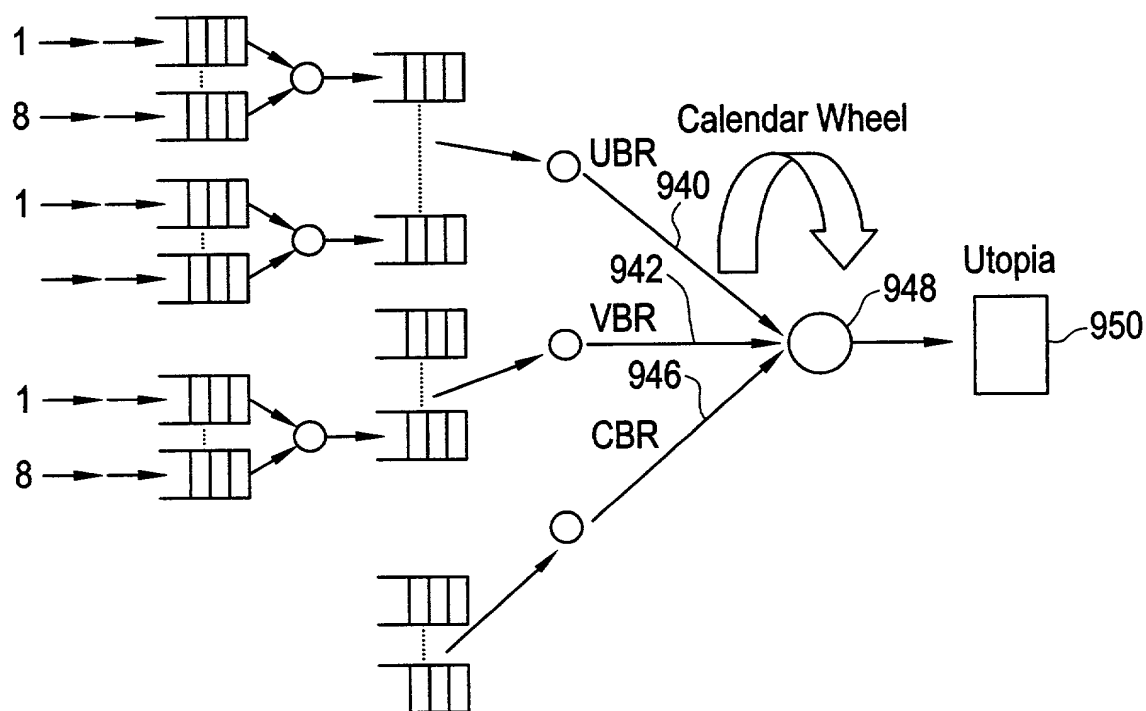


FIG. 76

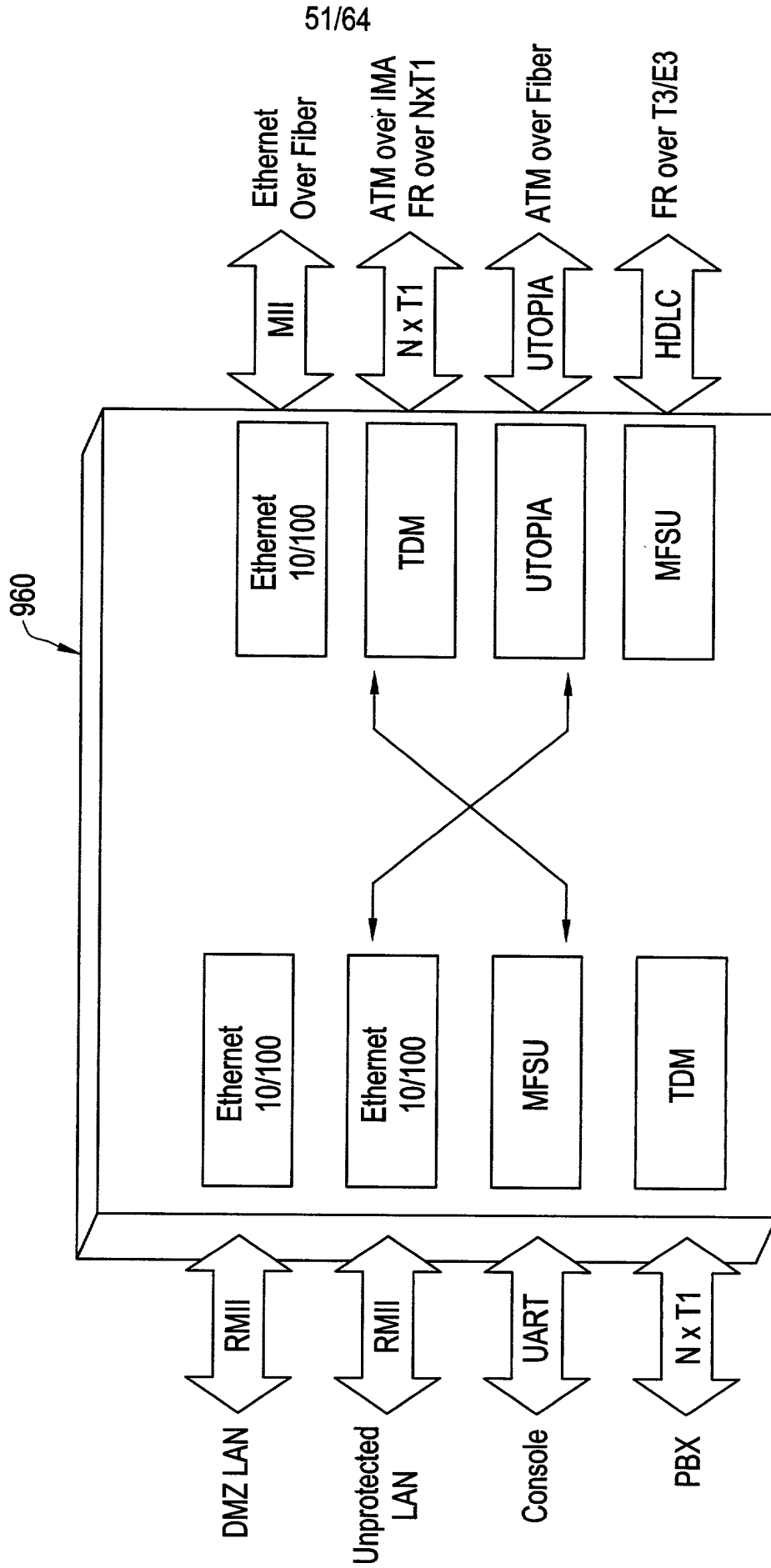




FIG. 77

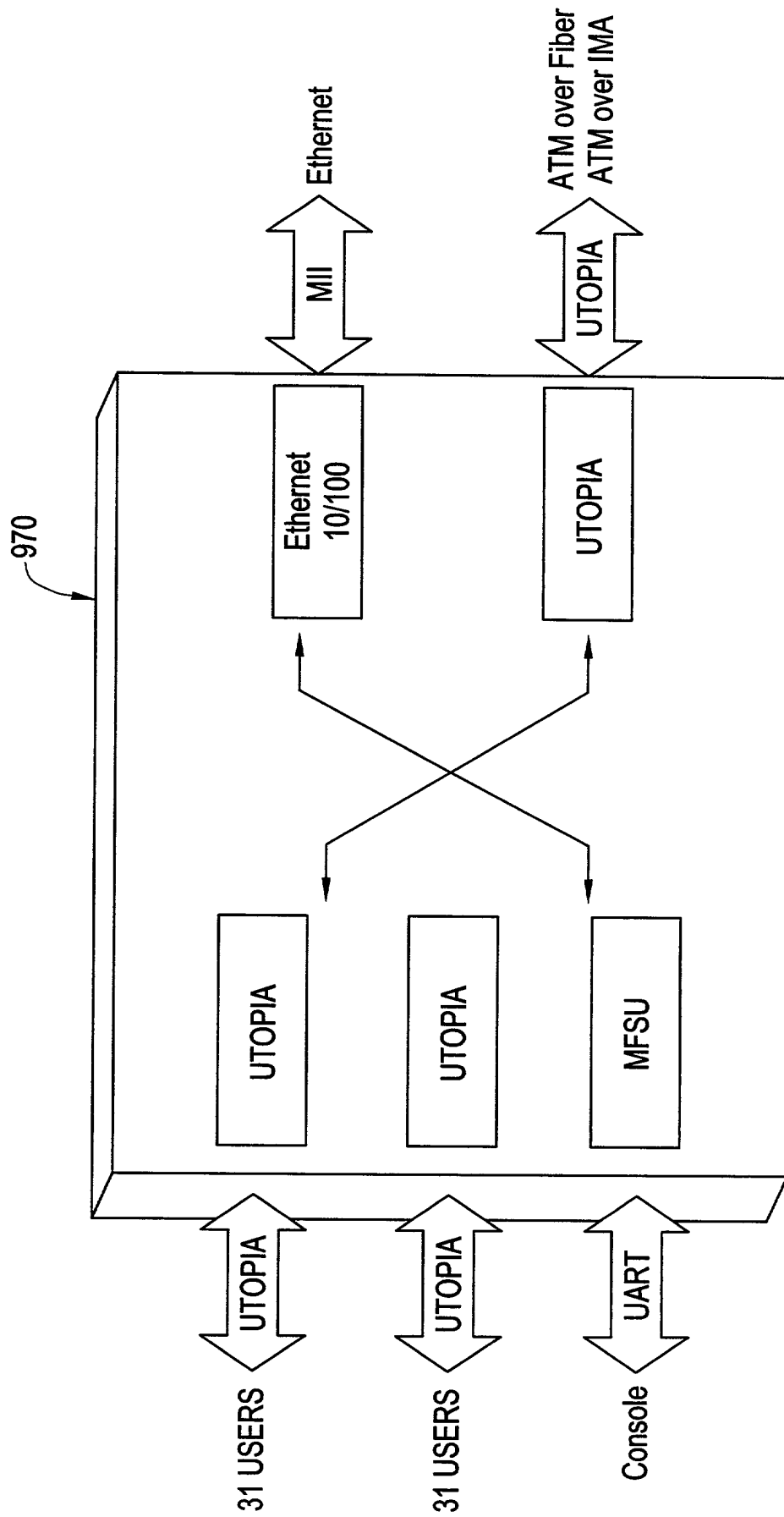


FIG. 78

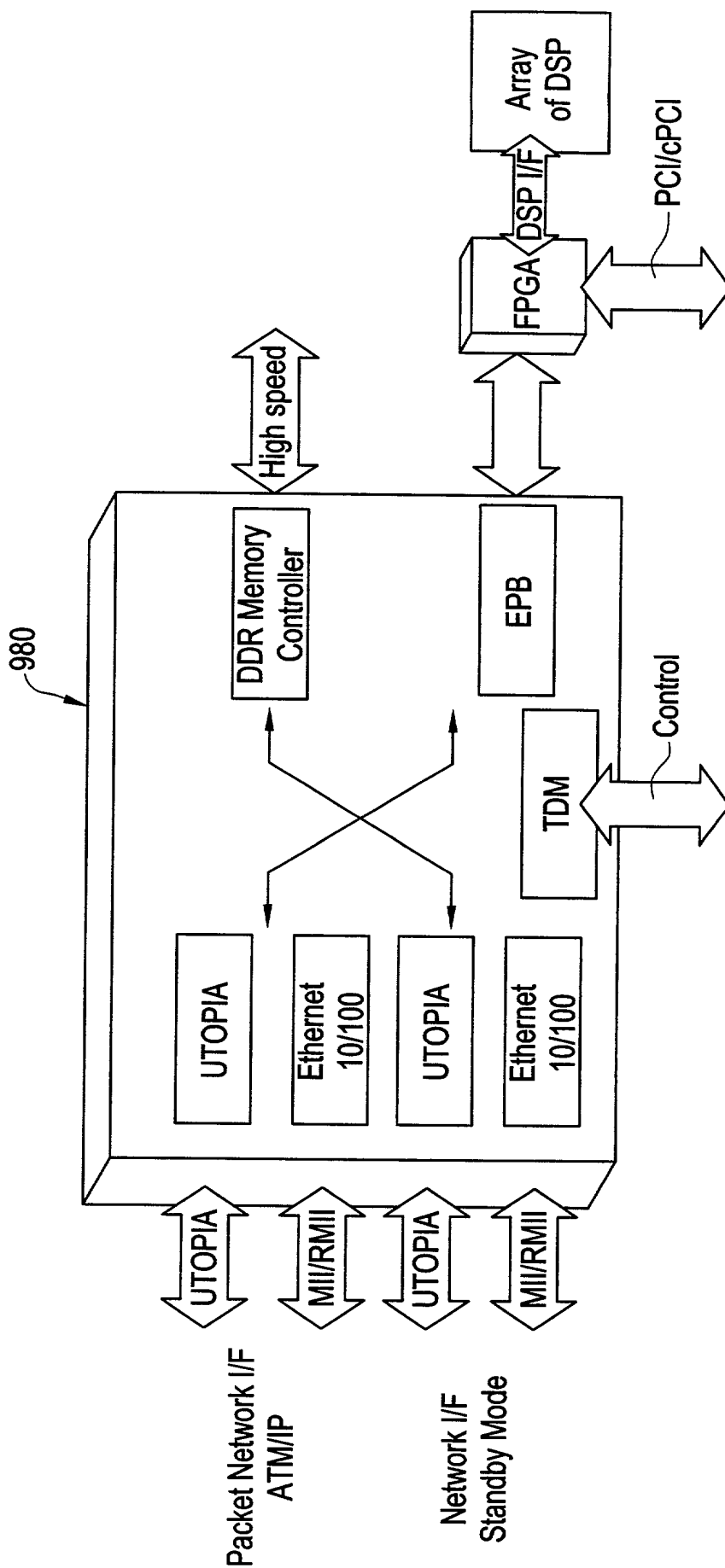
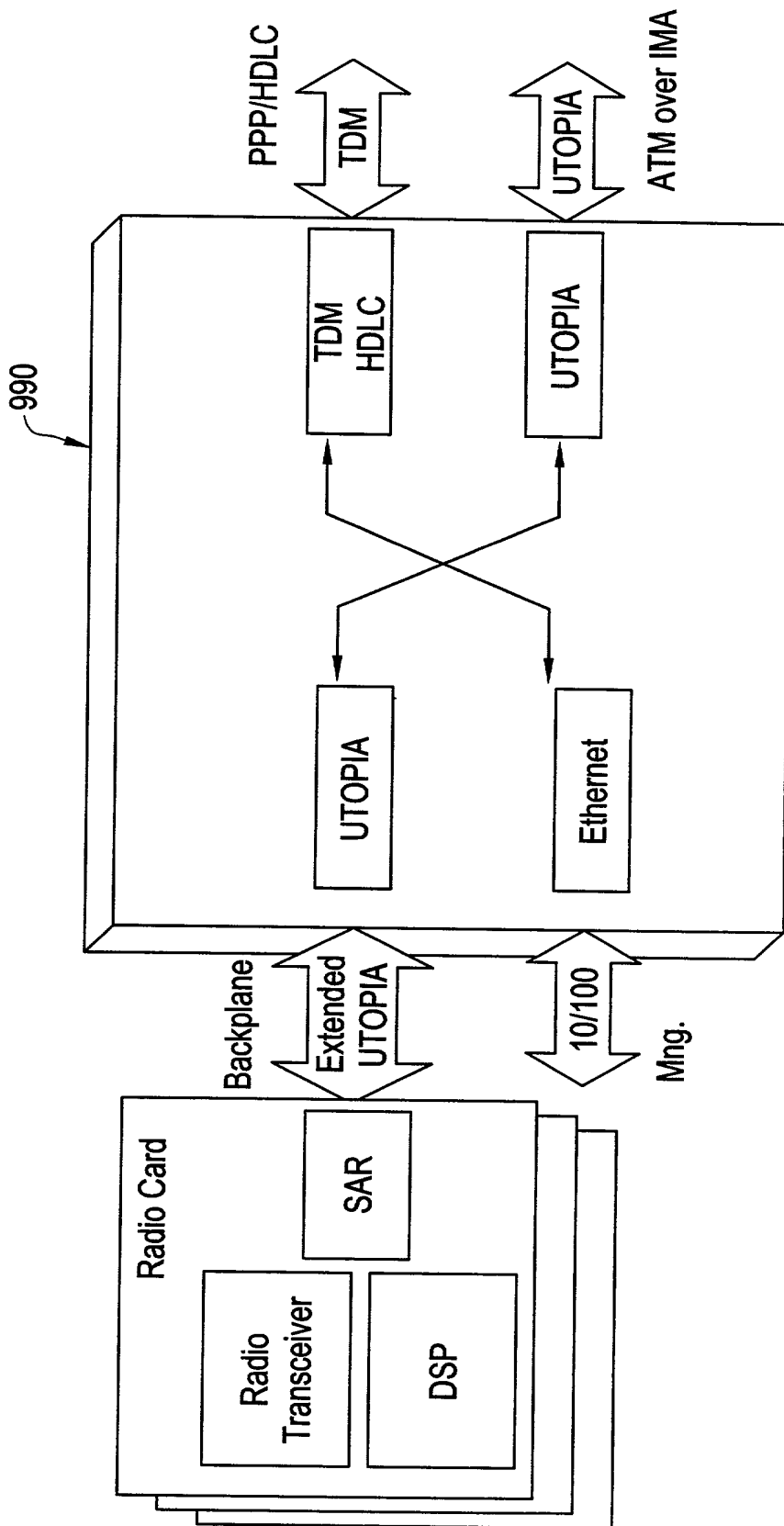




FIG. 79





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FIG. 80

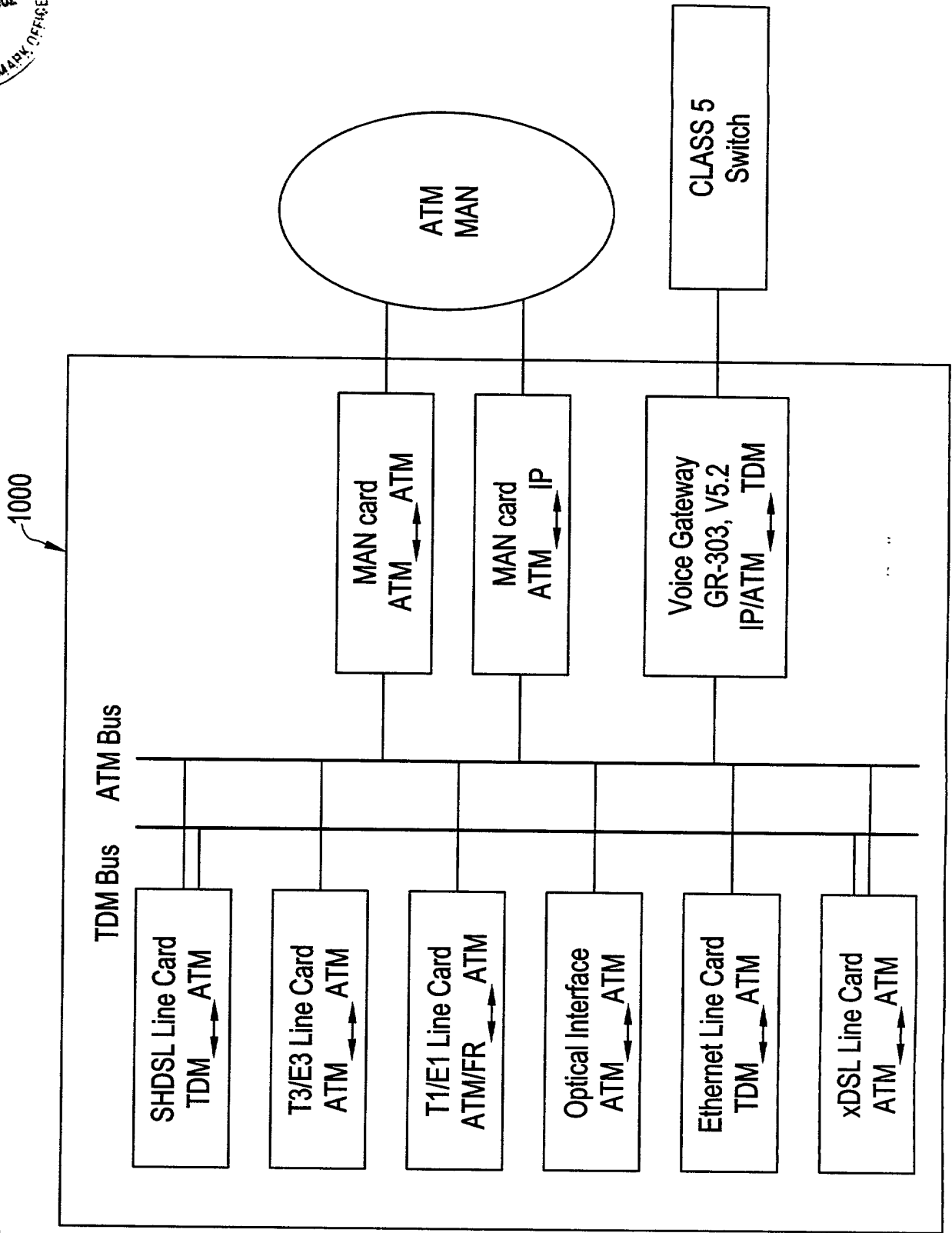


FIG. 81

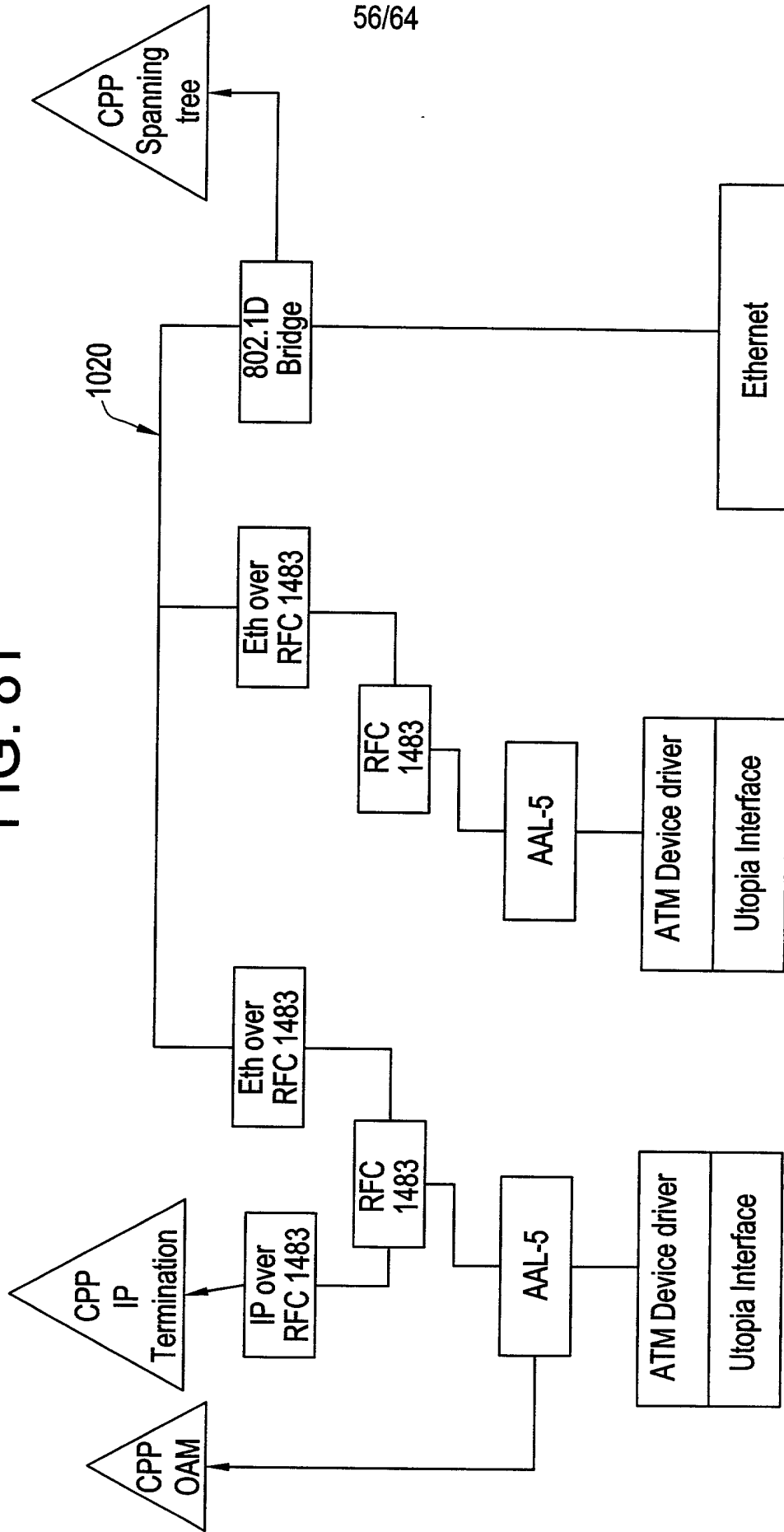


FIG. 82

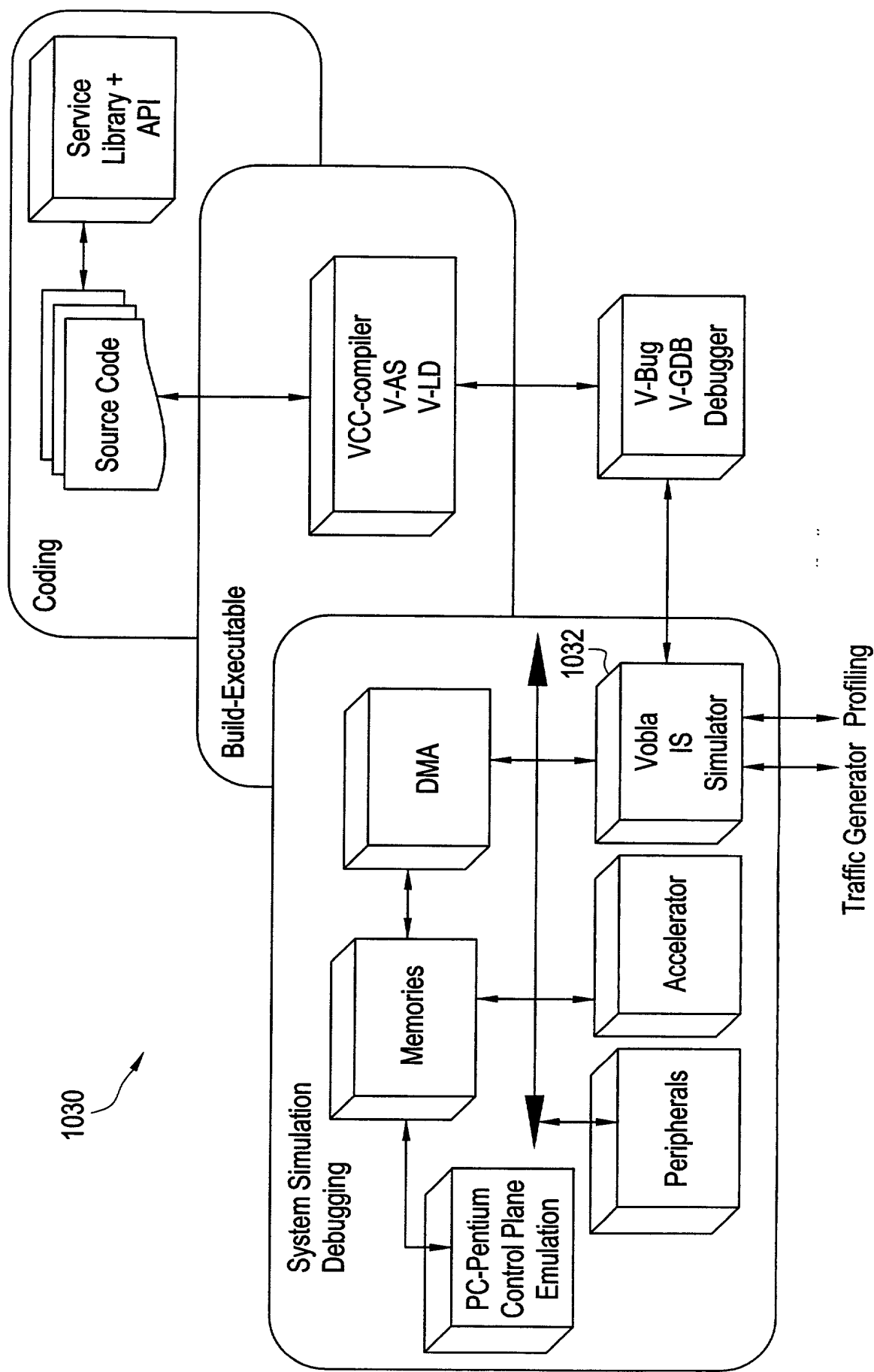


FIG. 83

1040

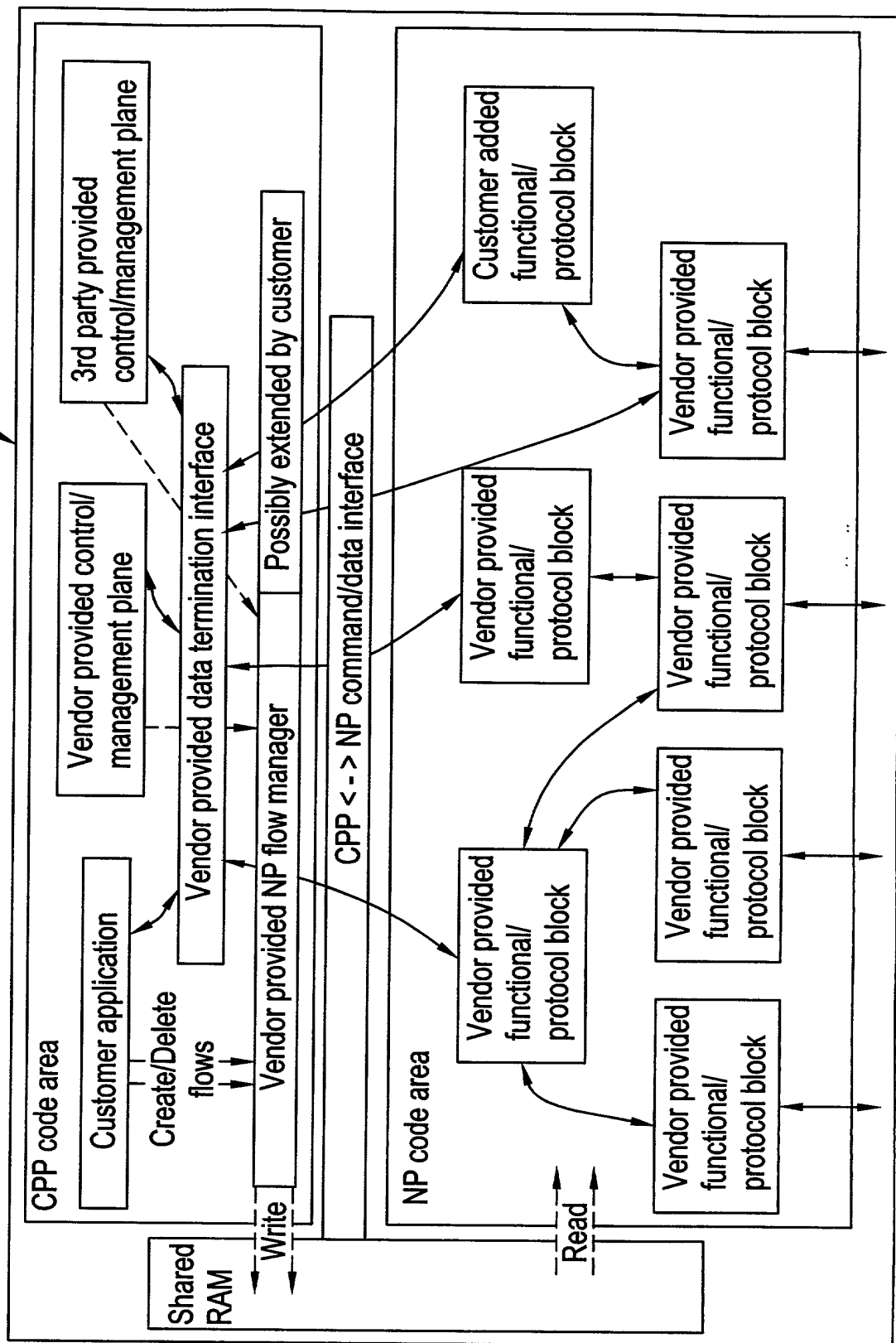
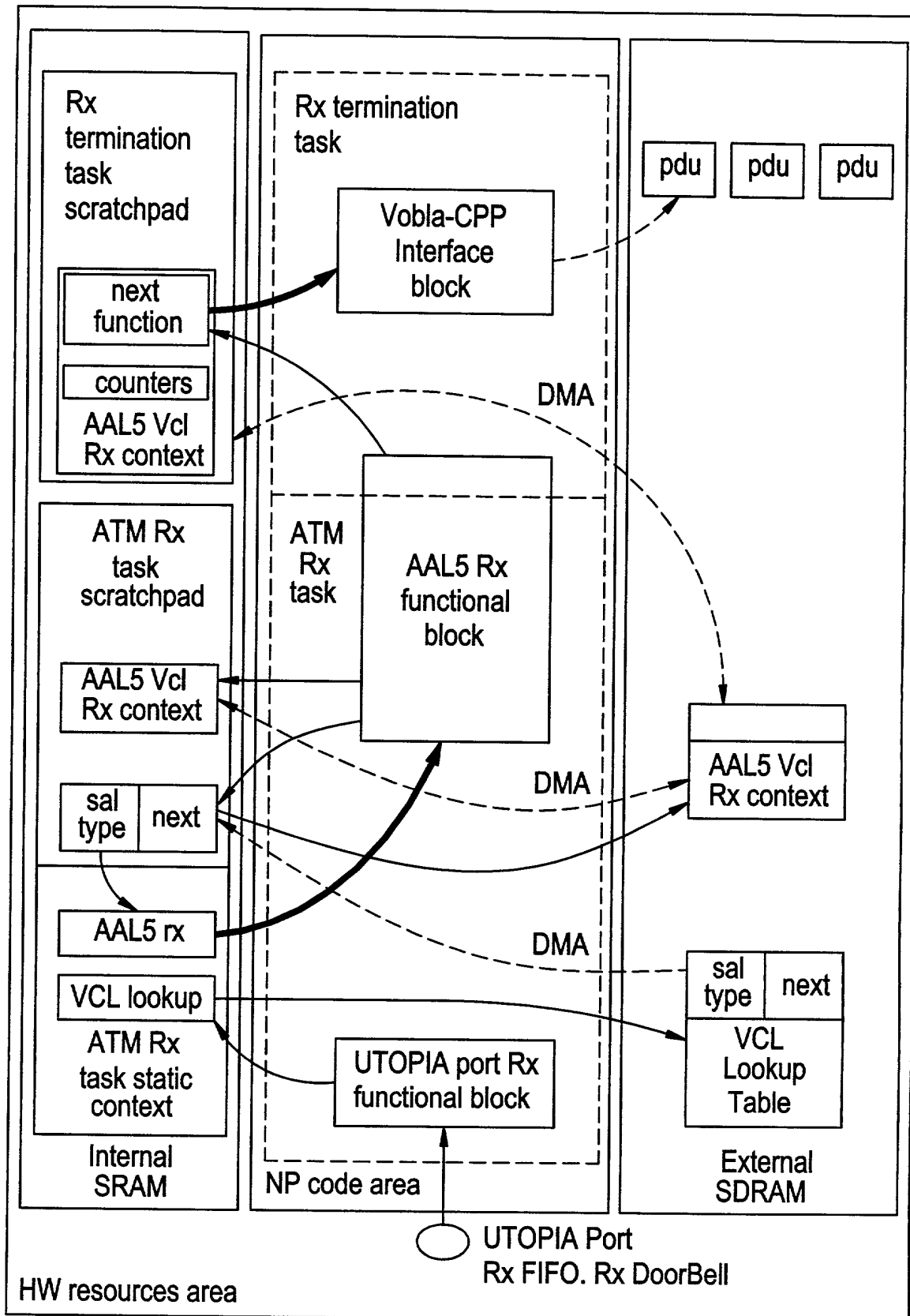


FIG. 84

1050



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FIG. 85

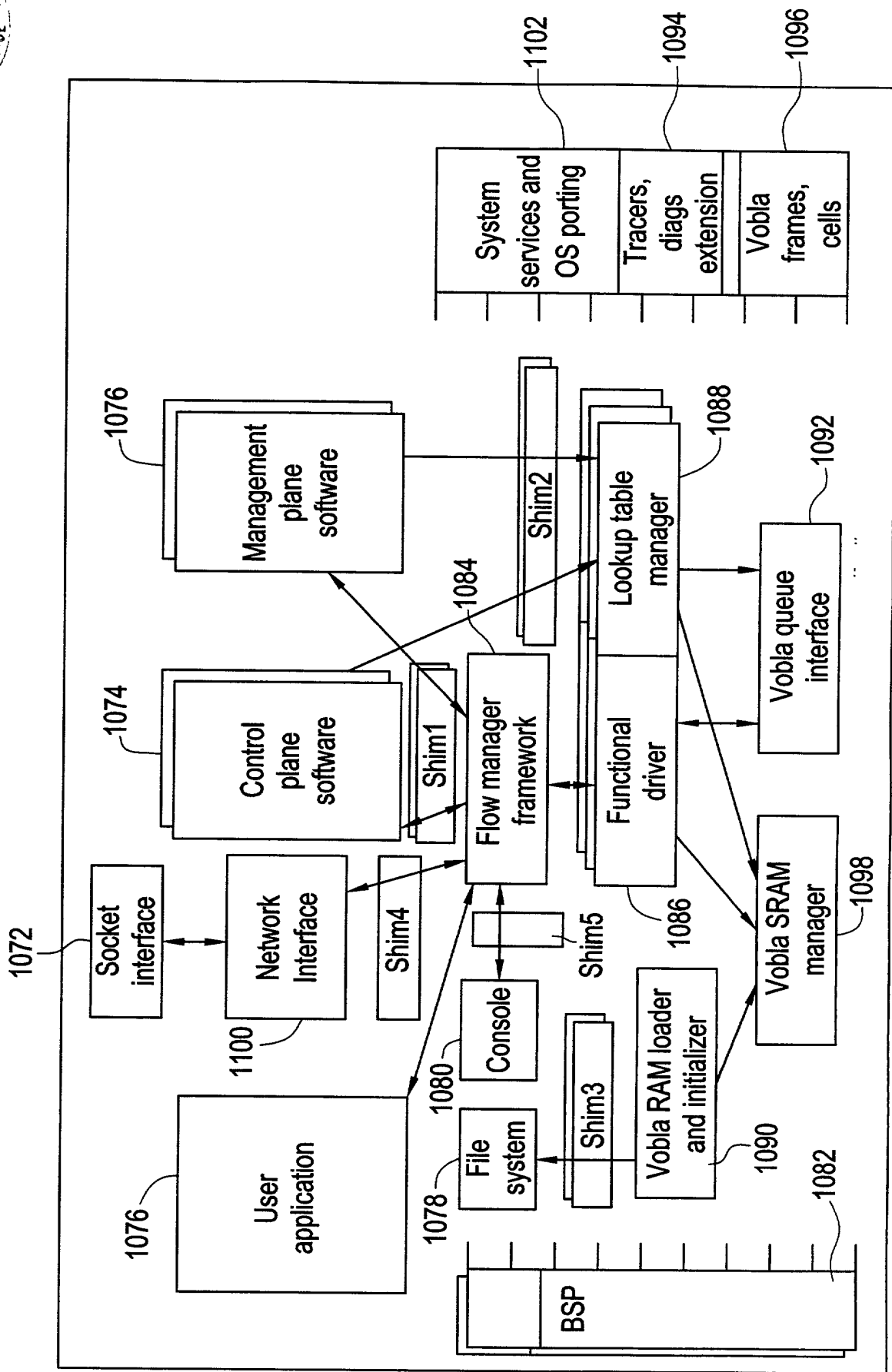
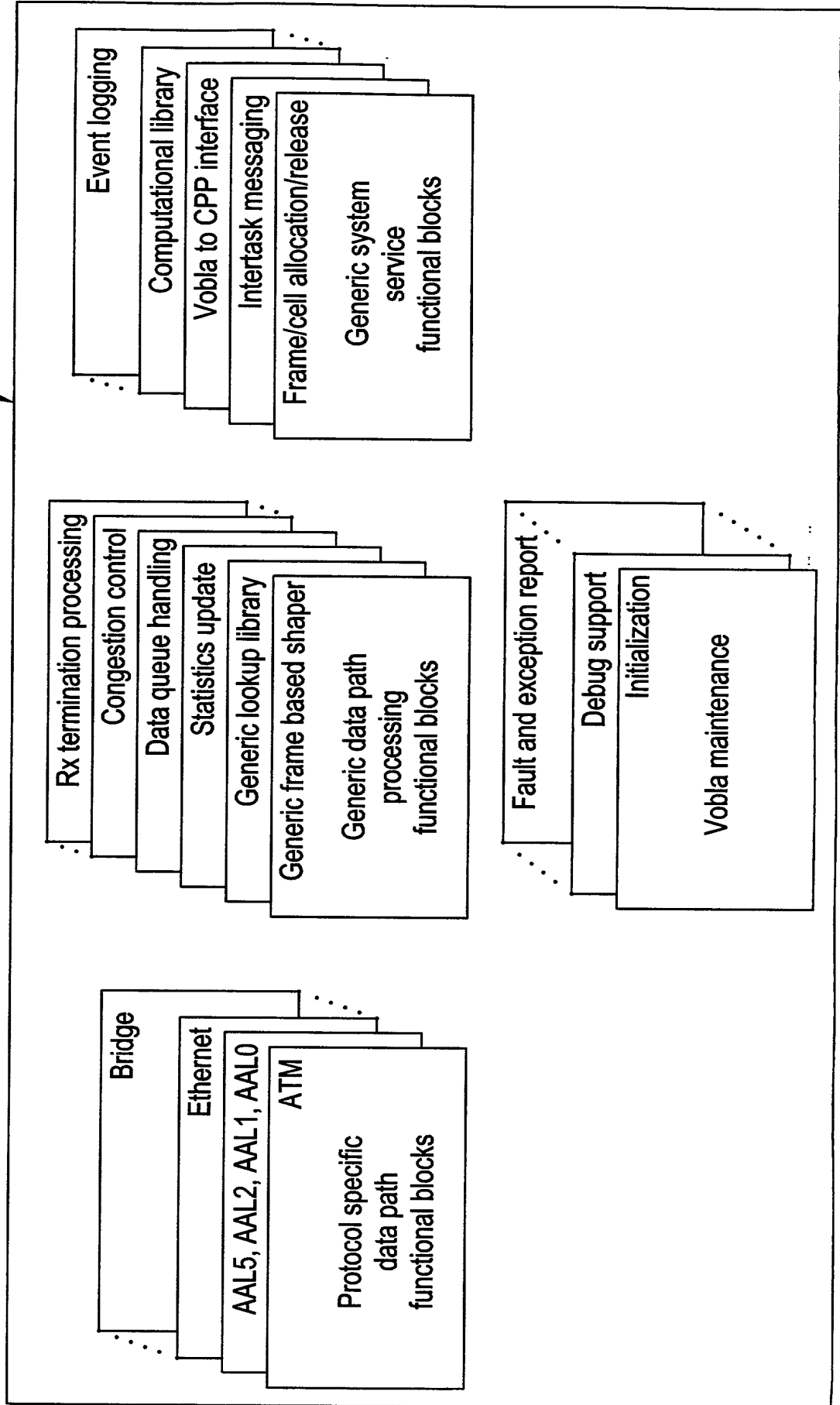


FIG. 86

1200

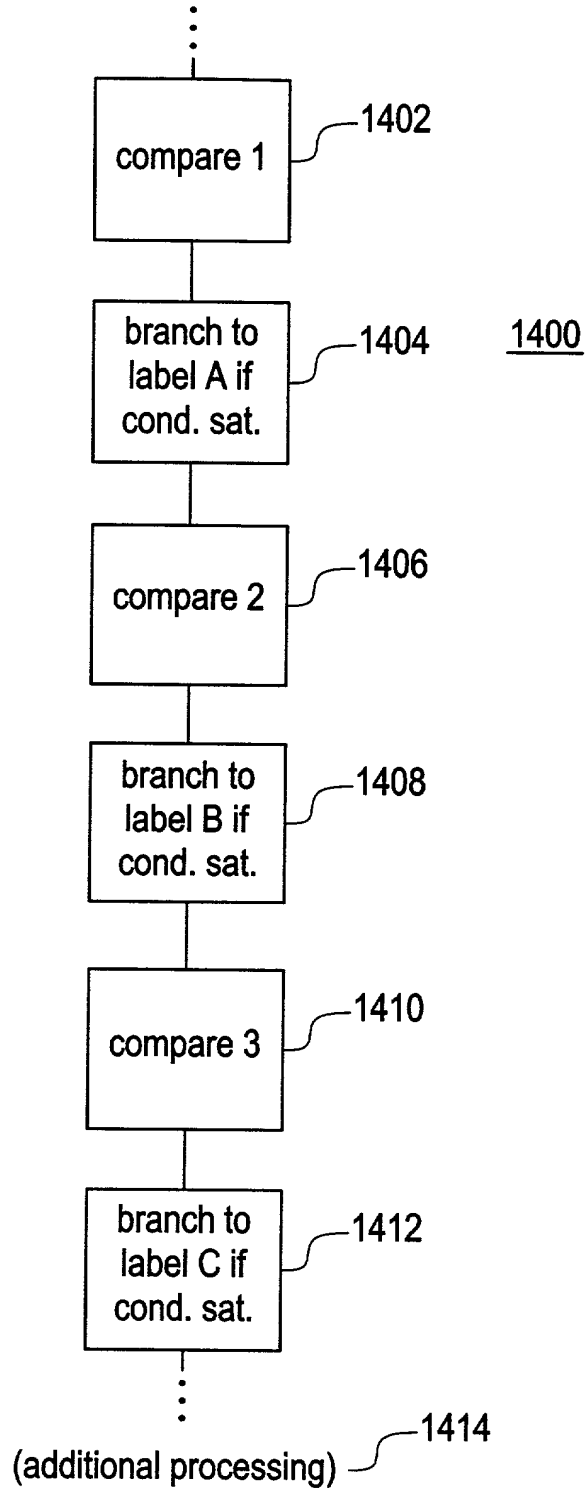




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FIG. 87

PRIOR ART



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FIG. 88

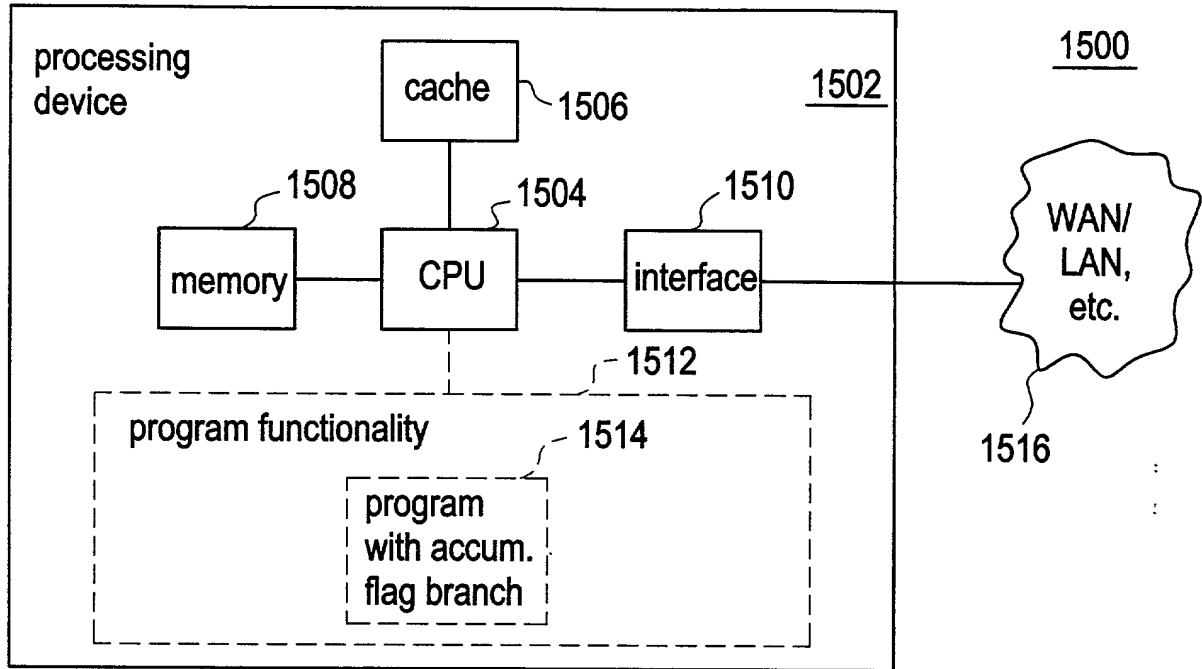


FIG. 89

